10/100 Ethernet 4-Port Switch

Defaults to
MII PHY mode
with 3.3V Levels

Auto MDIX is supported
Polarity Correction also supported

Requires Reset# asserted for 10 ms after power.
**Power Supplies**

**#1 3.3V Power Supply**

- Up to 2000 mA

**Power Sequencer**

- 4.1V nominal step point
- 2.5 MHz sw. freq.
- 22 uF minimum
- 50 uF max.

**#2 1.8V Regulator**

- 1.8V
- 2.5 MHz sw. freq.
- 22 uF minimum

**#3 CPU Core Supply**

- 1.0V or 1.12V

- 40 MHz CPU needs
  - 1.0V
  - 1000 MHz needs 1.12V

**#4 DDR3 1.5V Reg.**

- 3.3V
- 2.5 MHz sw. freq.
- 4 ms delay
- 40 ms delay

- 1.5V typ.
- 2.5%

**Analog**

- 1.8V
- 2.5 MHz sw. freq.
- 22 uF minimum
- 50 uF max.

**Power Supply**

- 3.3V Power Supply up to 2000 mA

- 2.5 MHz sw. freq.
- 22 uF minimum
- 50 uF max.

**#1 3.3V Power Supply**

- 4.1V nominal step point
- 2.5 MHz sw. freq.
- 22 uF minimum

**Power Sequencer**

- 4 ms delay
- 40 ms delay

**#2 1.8V Regulator**

- 1.8V
- 2.5 MHz sw. freq.
- 22 uF minimum

**#3 CPU Core Supply**

- 1.0V or 1.12V

- 40 MHz CPU needs
  - 1.0V
  - 1000 MHz needs 1.12V

**#4 DDR3 1.5V Reg.**

- 3.3V
- 2.5 MHz sw. freq.
- 4 ms delay
- 40 ms delay

- 1.5V typ.
CPU Power

PXA168 PCIe rails must be connected to AN_1.8V

CPU

Technologic Systems

Date: June 27, 2013

Title: TS-4720 CPU Power and Bypass caps

Rev: A

Designer

Sheet 7 of 10
Micro SD Card Socket

RTC and Temp. Sensor

OFF BD.
SD Card Power

3.3V --> 5V
Level Shifter

SD Card LEDs
PCIe 100 MHz
Clock Generator

CPU

PCIe

CPU
Debug UART

Reboot Flag

PXA166 and PXA168 BOM Differences

R57 = 140 ohm for PXA168
R56 removed for PXA166
FB6, FB7 removed for PXA166
U24 removed for PXA166
U2 = PXA166 Commercial Temp
or = PXA168 Industrial Temp