Control
Schmitt Trig. on all Inputs
10 MHz min clock for PLL
No Internal clock

All pins have prog. PU resistor
Two 100-pin Off-board Connectors

"5V" pins supply all power to the module. Apply 4.5V to 5.5V to these pins.

OFF_BD_RESET# is an Output used to reset all peripherals.

EXT_RESET# is an Input used to reboot the CPU. Do not drive active high (not open drain).

"5V" pins supply all power to the module. Use to reset all peripherals.

Bus Control

If Bus is not needed, all Bus signals can be changed to DIO.

Devices connected to this bus must never drive it when BUS_CM is deasserted (must be off within 30 nS of deassertion).

Devices must pull the BUS_NGW pin low if they need more than 150 nS strobe.

The data bus cannot have more than 30 pF of off-board capacitive loading. May need data buffer chip for heavy loads.

CN1-Pin 67 = UART0 TXEN

CN2-94 = Aud TXD

Technologic Systems Date: May 21, 2014
Title: TS-4900 Off-board Connectors
Rev: A Designer
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