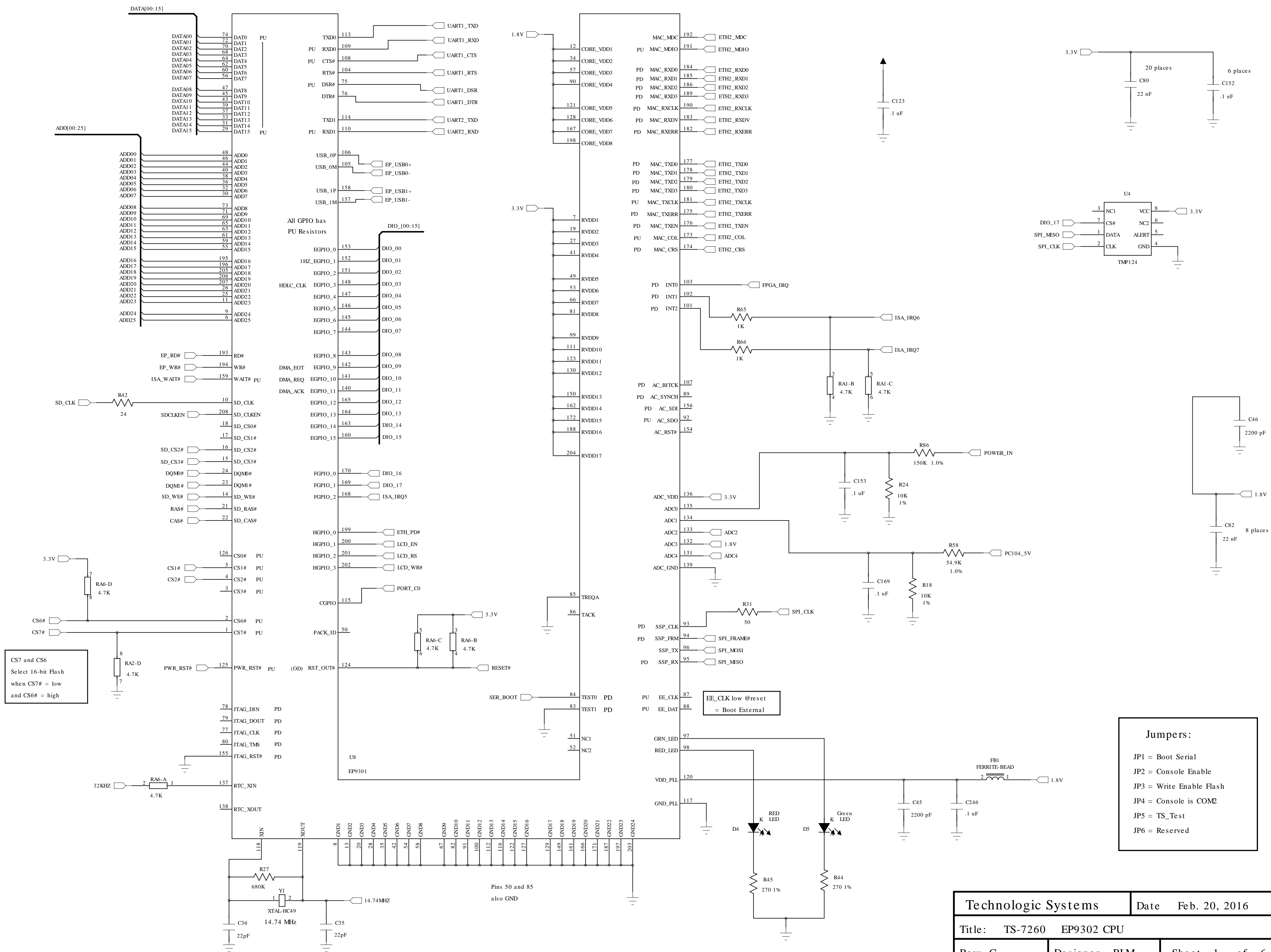


# Changes made from Rev.B to Rev.C

- 1) Changed SD card socket due to EOL issue
- 2) Changed U3 regulator to a higher current version
- 3) Changed U12 from Exar SP3239 to ST3237EBPR  
This allows the RS-232 ports to run at higher bit rates



CS7 and CS6  
Select 16-bit Flash  
when CS7# = low  
and CS6# = high

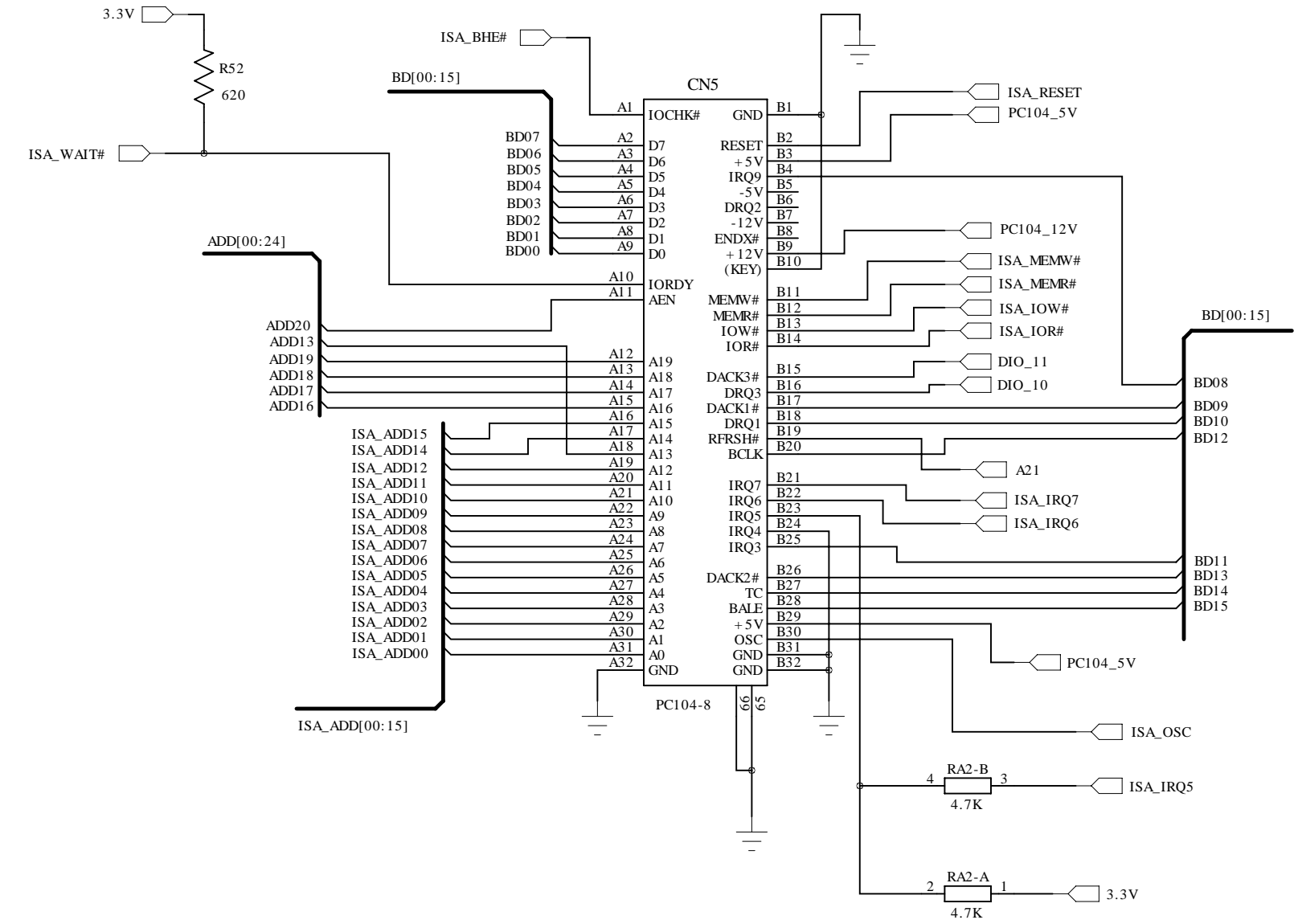
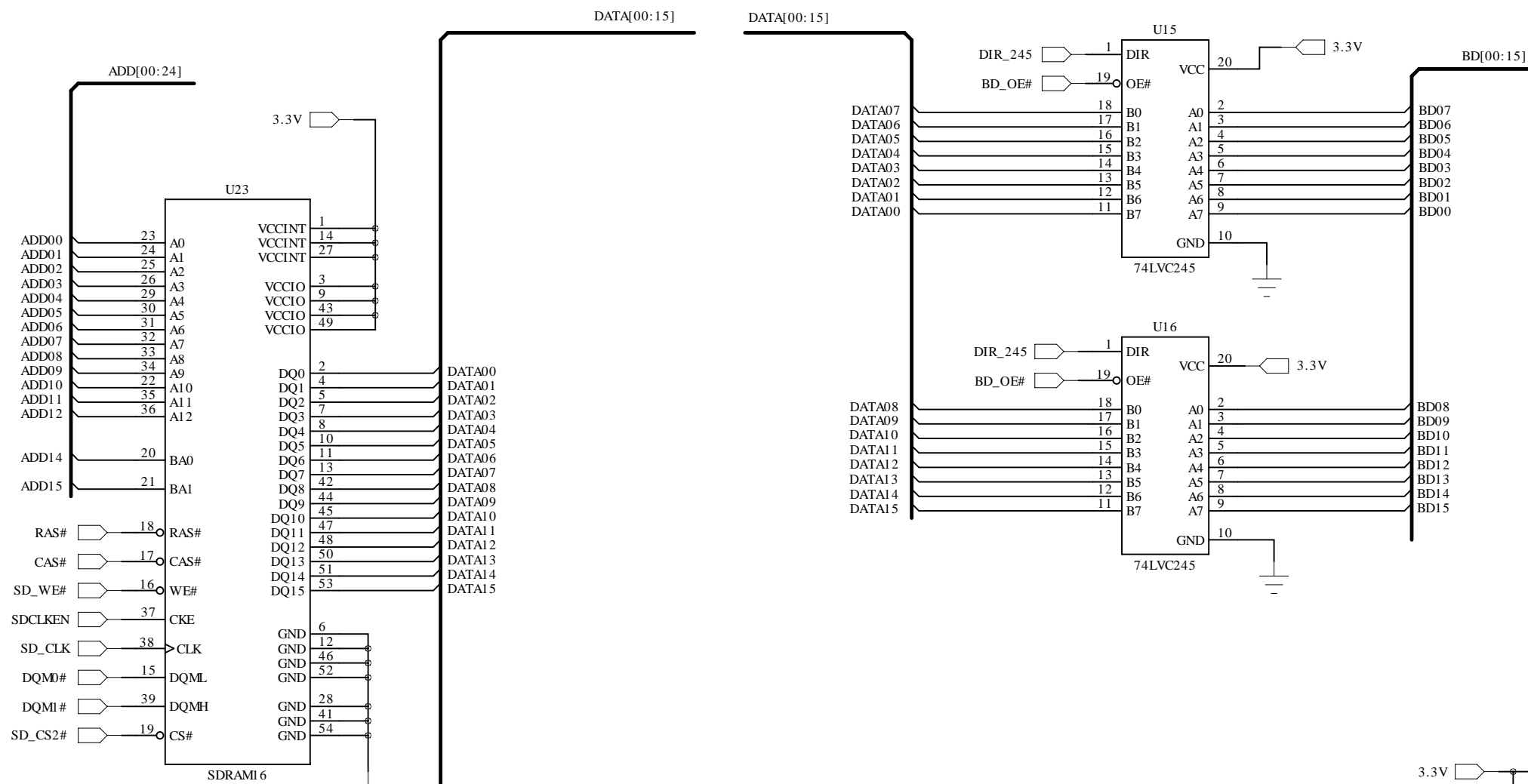
EE\_CLK low @reset  
= Boot External

**Jumpers:**

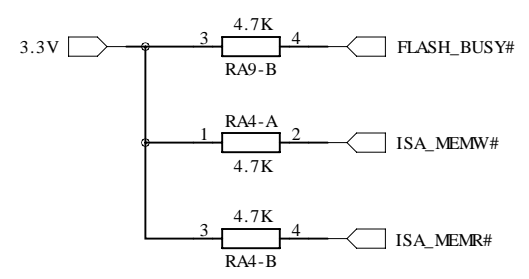
- JP1 = Boot Serial
- JP2 = Console Enable
- JP3 = Write Enable Flash
- JP4 = Console is COM2
- JP5 = TS\_Test
- JP6 = Reserved

# PC/104 Connector

## SDRAM

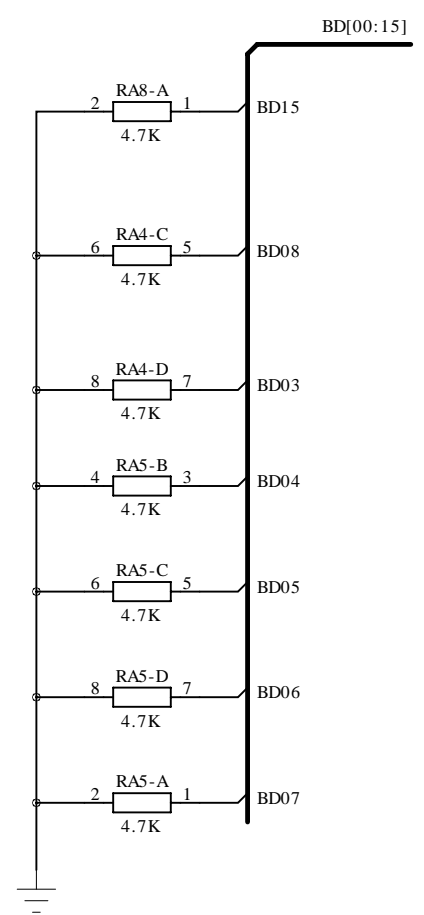
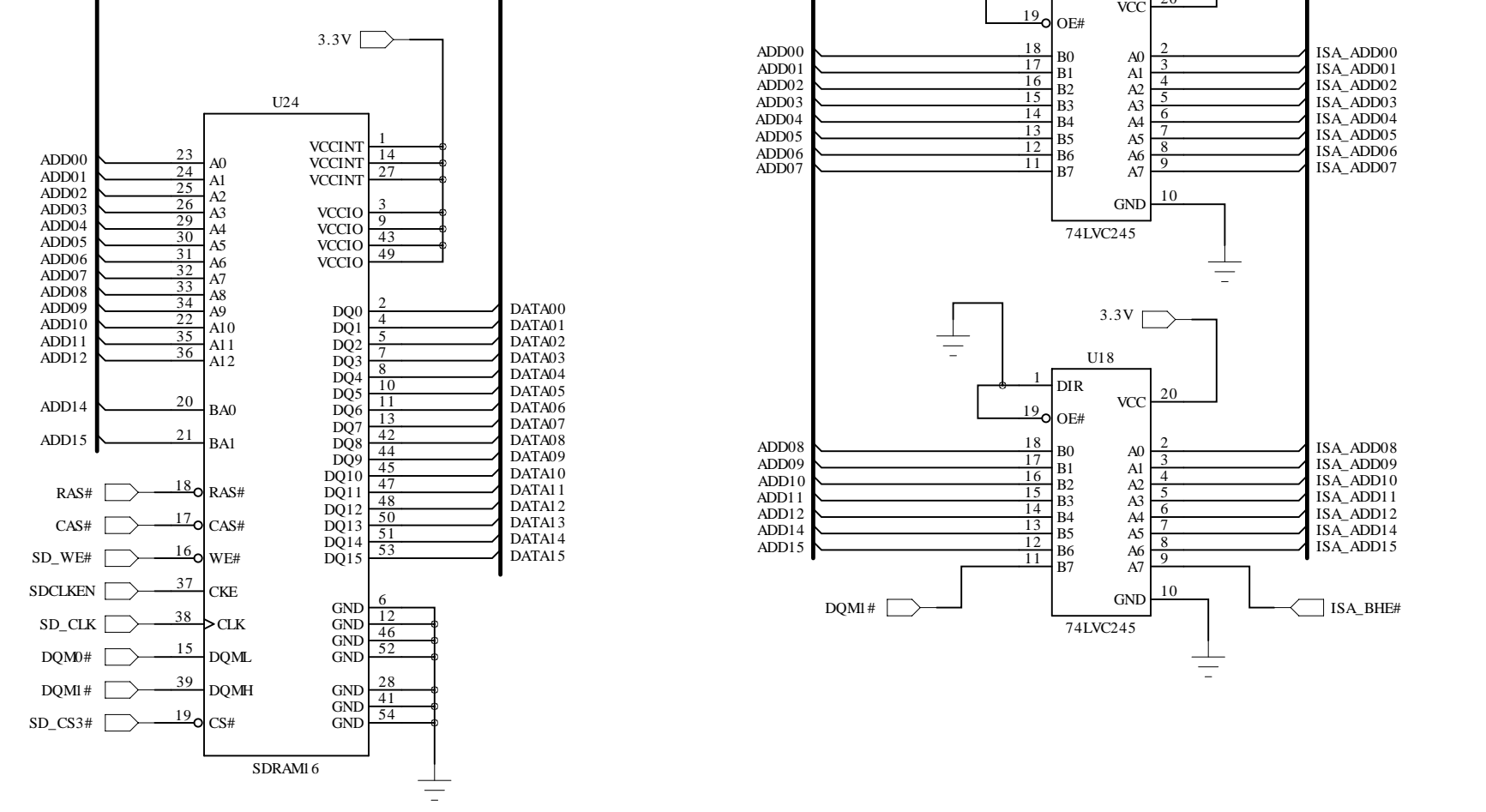


IRQ6 and IRQ7 have Resistor pull downs

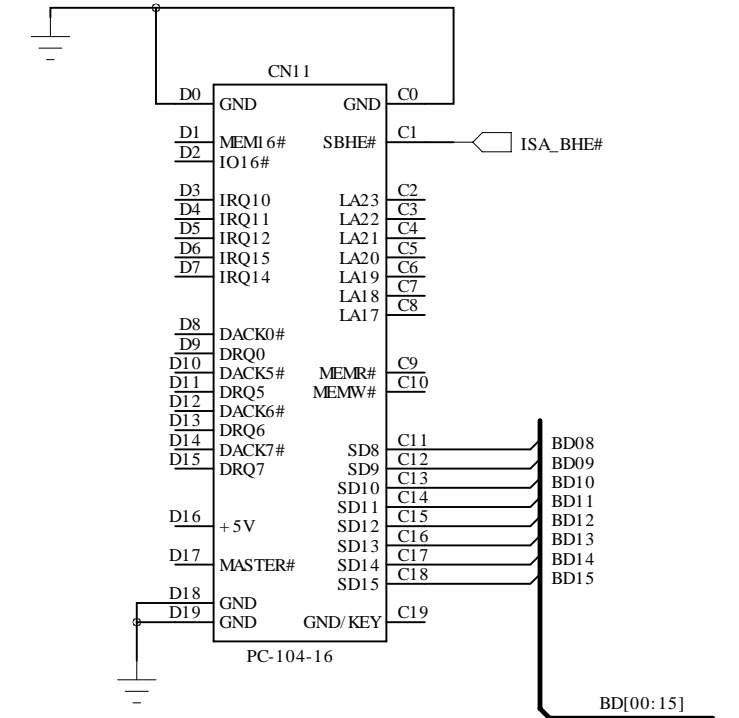


# PC/104 40-pin Connector

## SDRAM



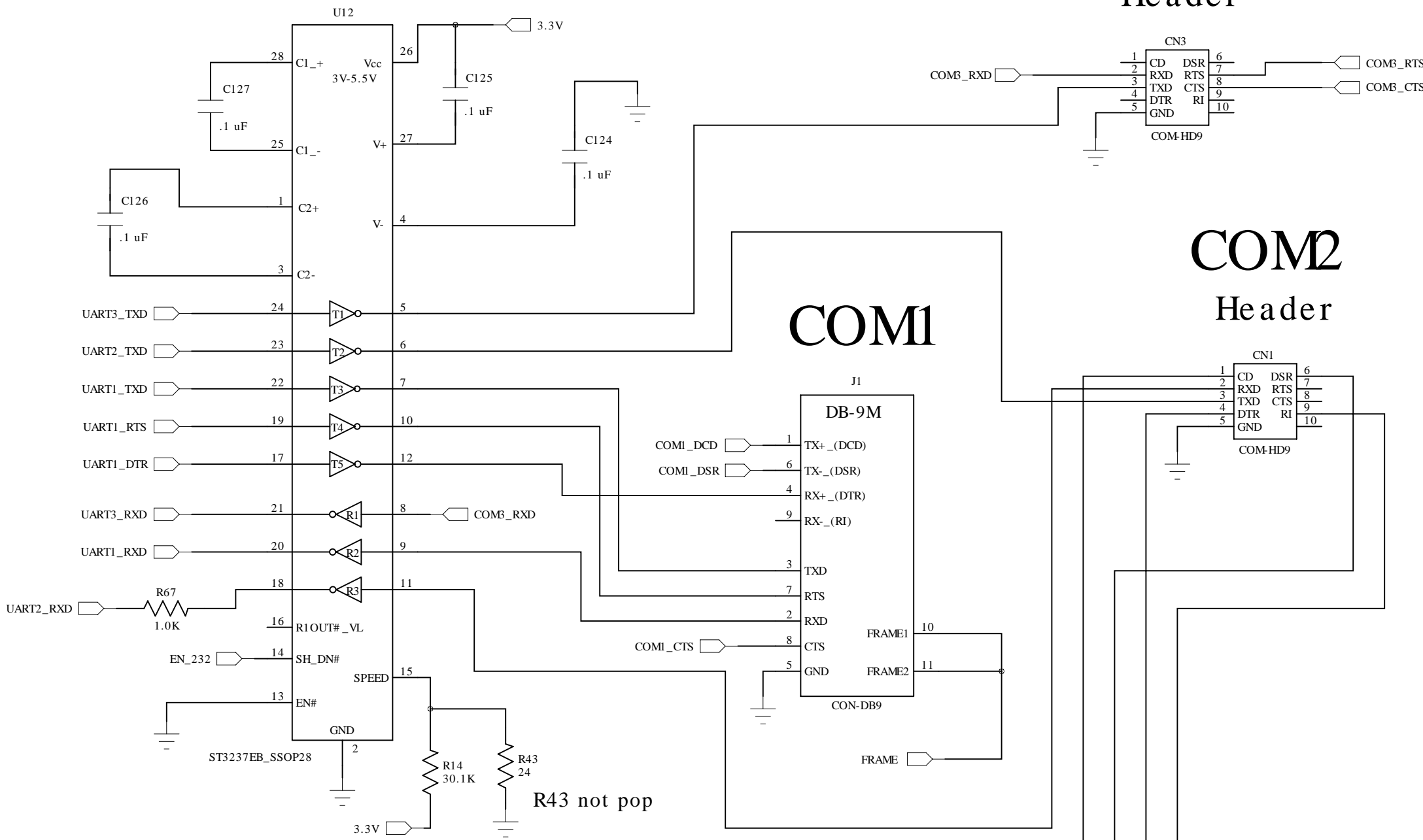
Data Bus Pull-Down Resistors



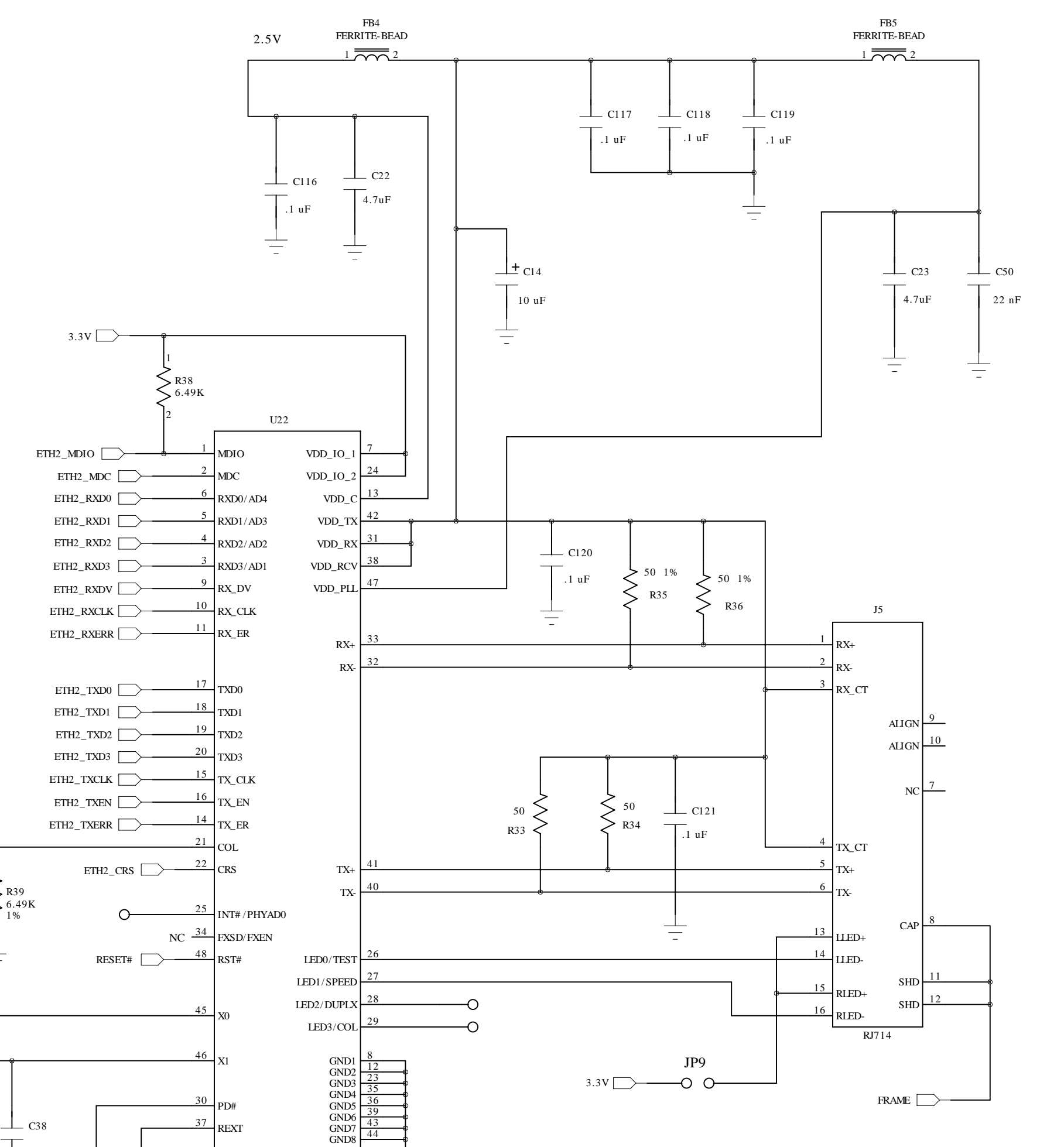
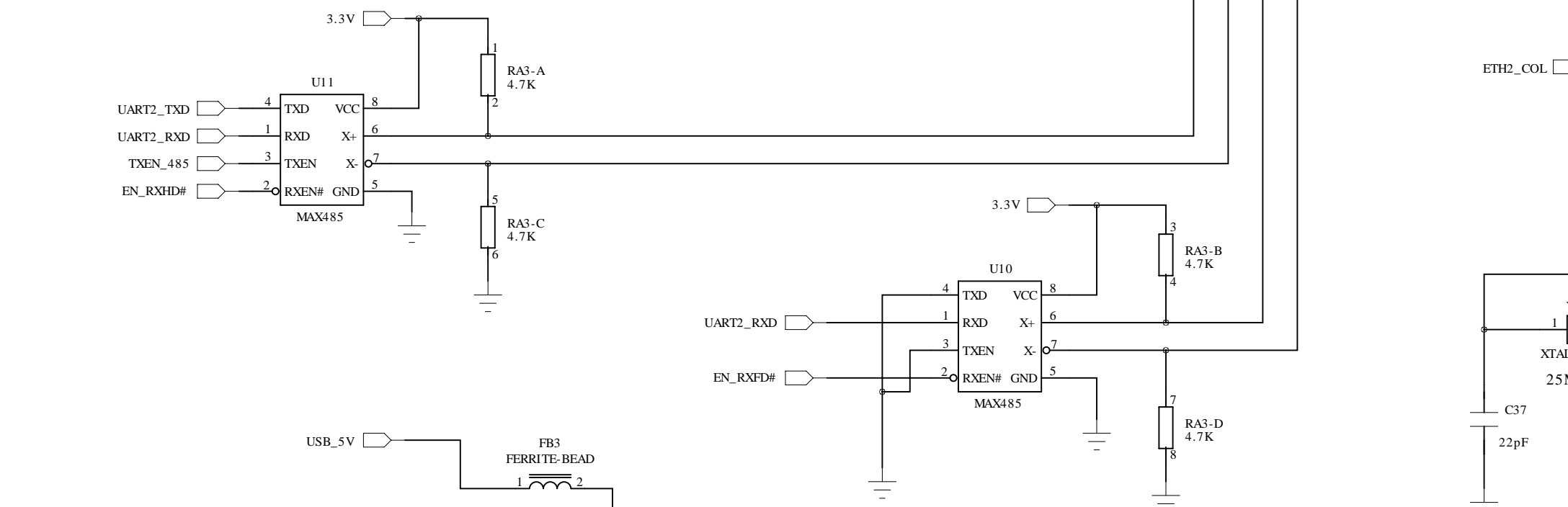
3.3V RS-232

COM3 Header

10/100 Ethernet

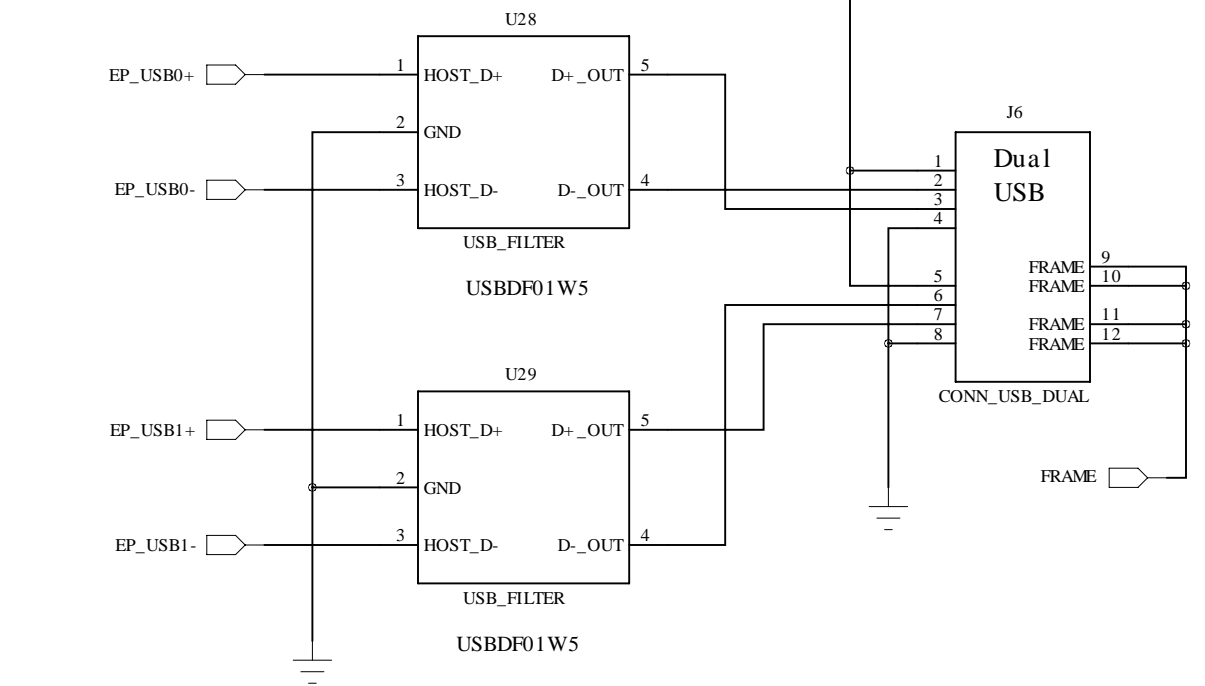


R43 installed causes slow slew on TX

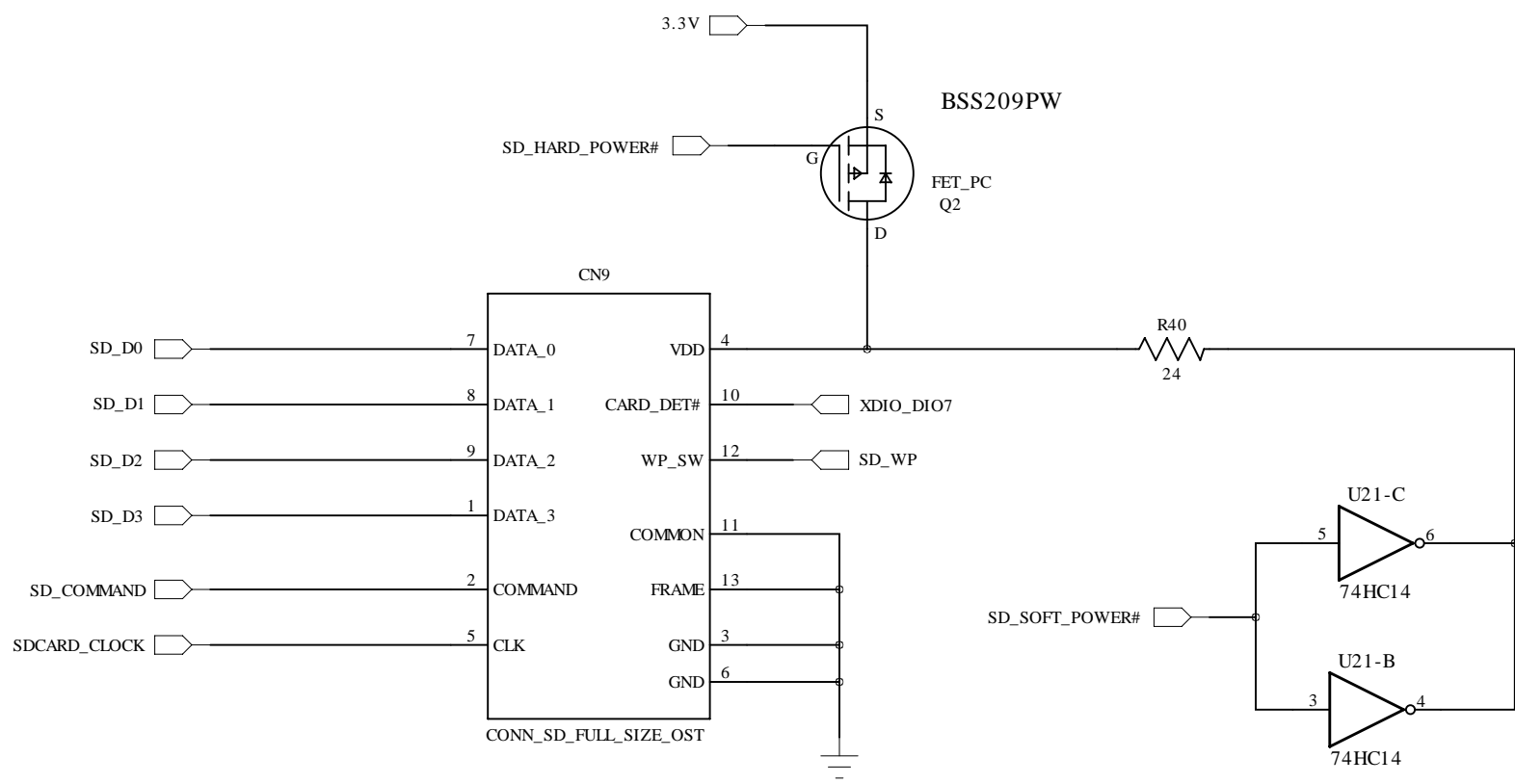


KS8721  
 KS8721BL = LQFP 48  
 KSZ8721BL = Lead-free  
 Add I suffix for Industrial temp  
 Defaults to PHY address 00001

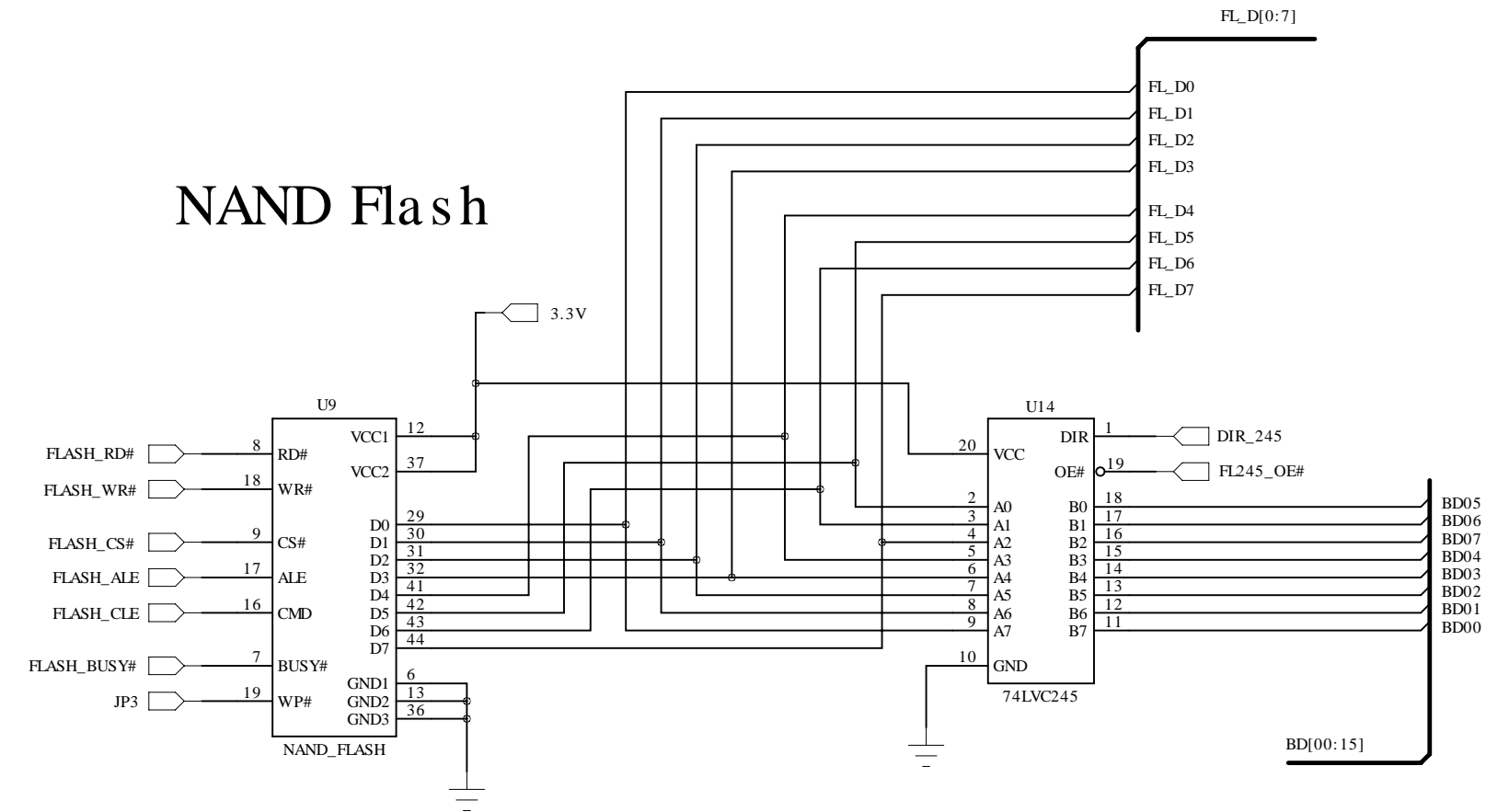
Right LED (Amber)  
 100 Mbit  
 Left LED (Green)  
 Link / Activity



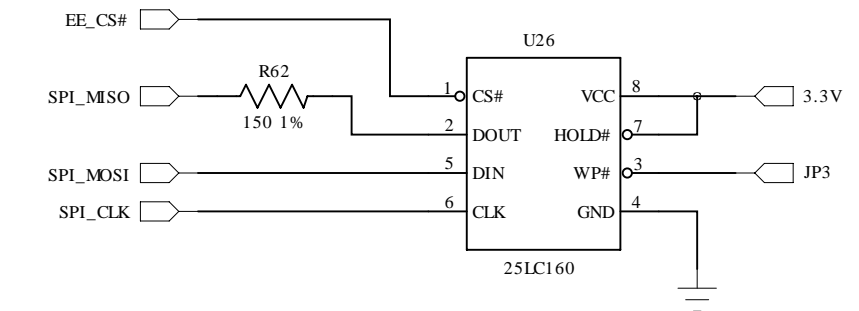
# SD Card Socket



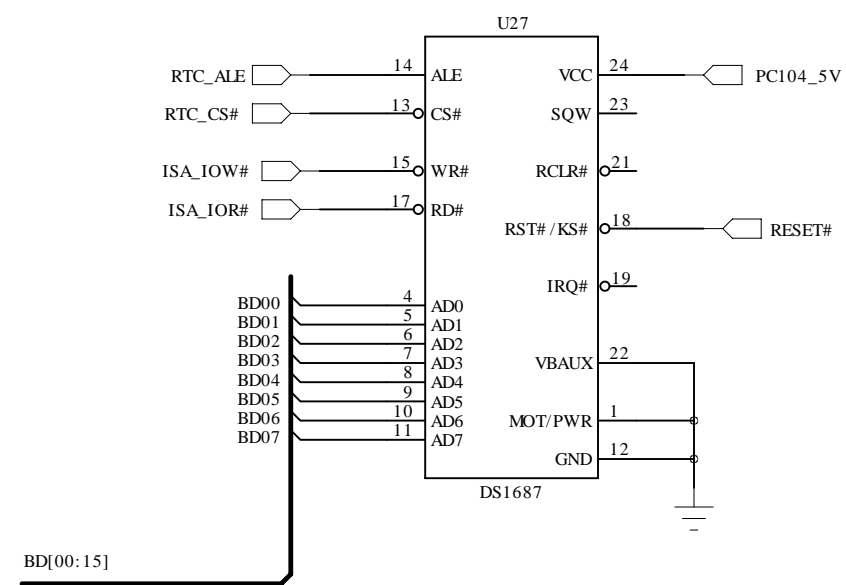
# NAND Flash



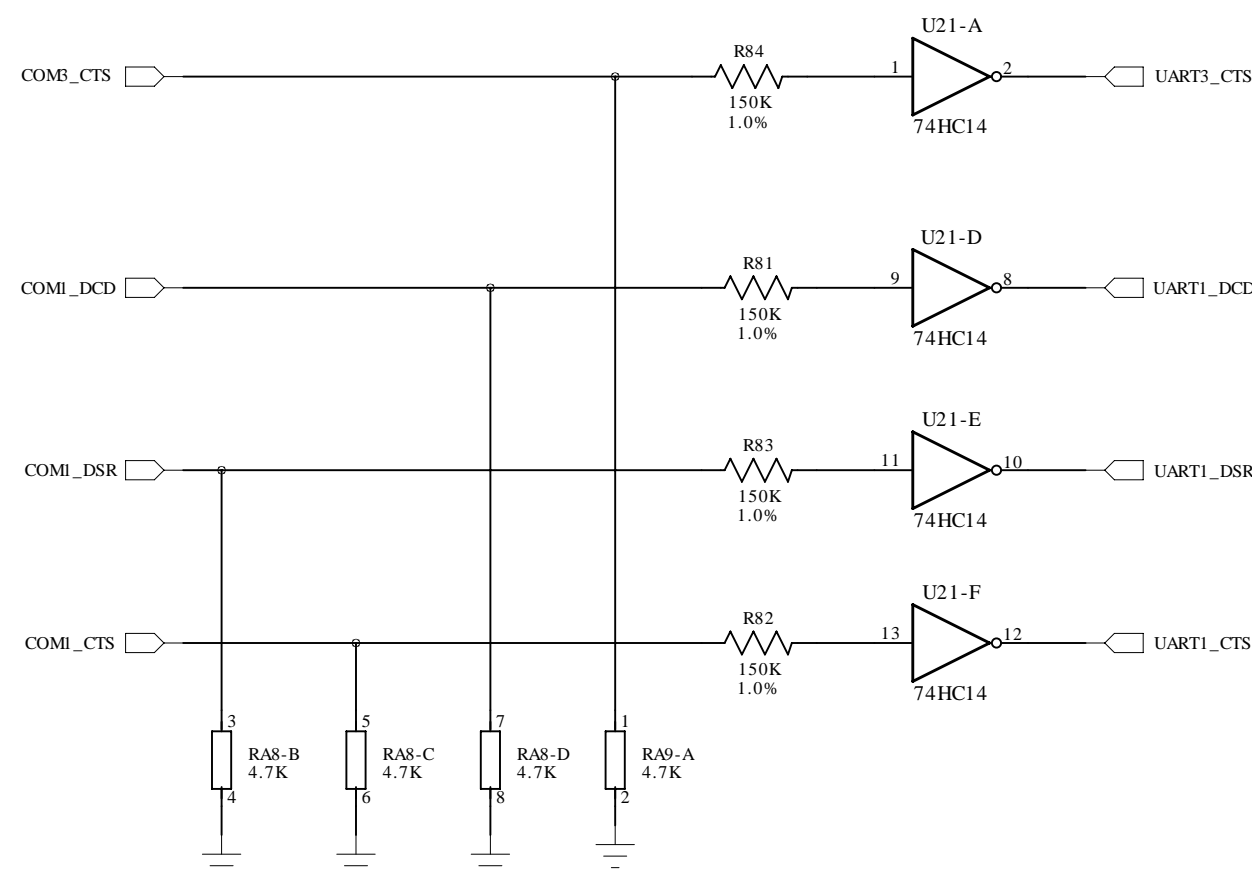
# Boot EEPROM



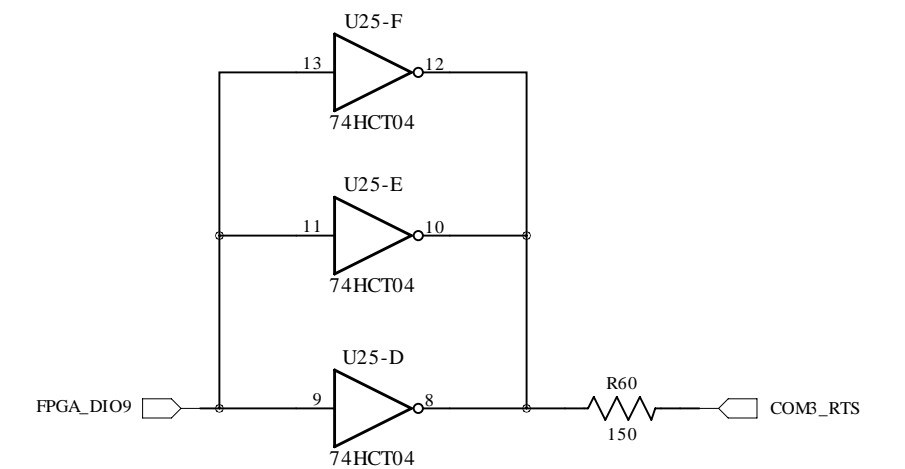
# Real Time Clock



# 3.3V Powered

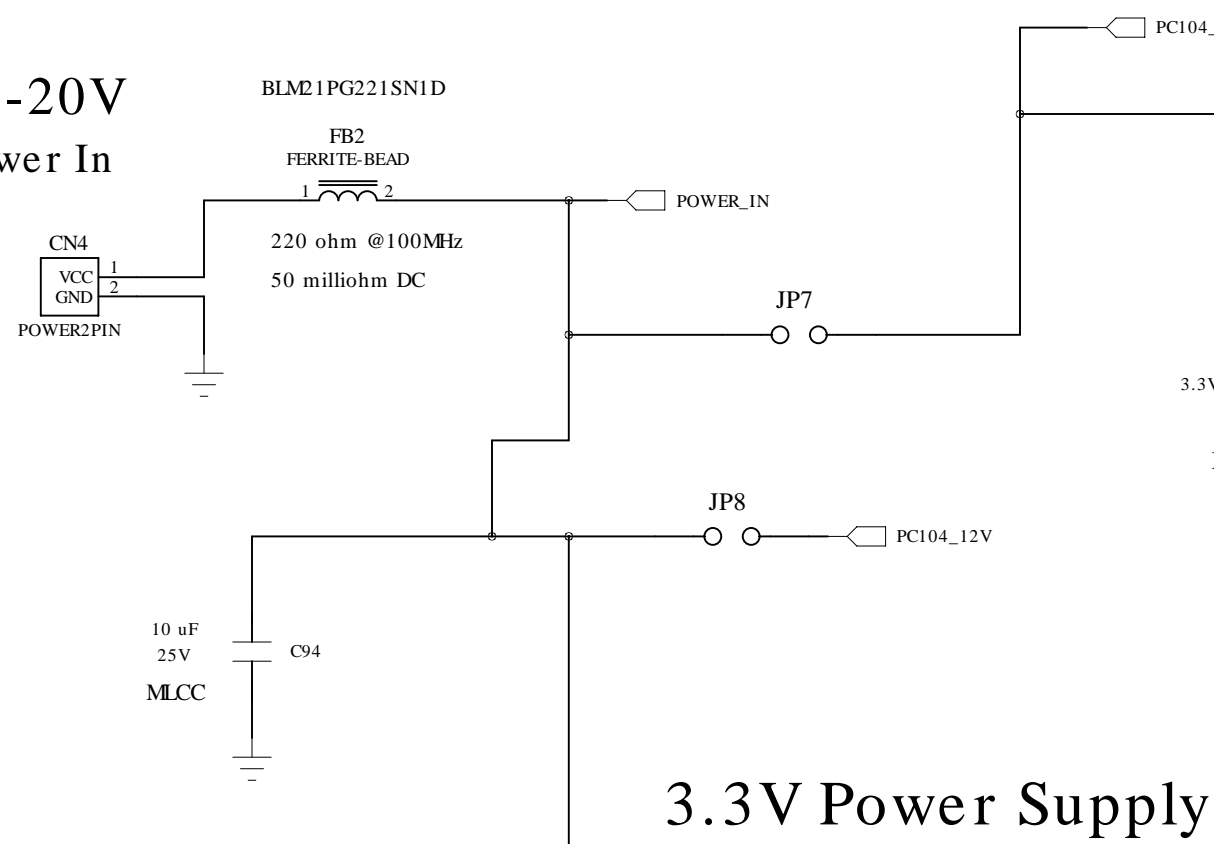


# 5V Powered

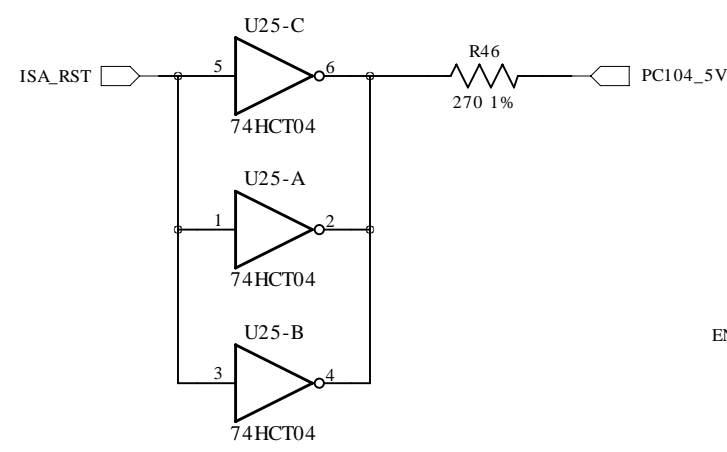


Technologic Systems	Date	Feb 20, 2016
Title: TS-7260 NAND Flash, EEPROM, RTC		
Rev: C	Designer	RLM
Sheet		4 of 6

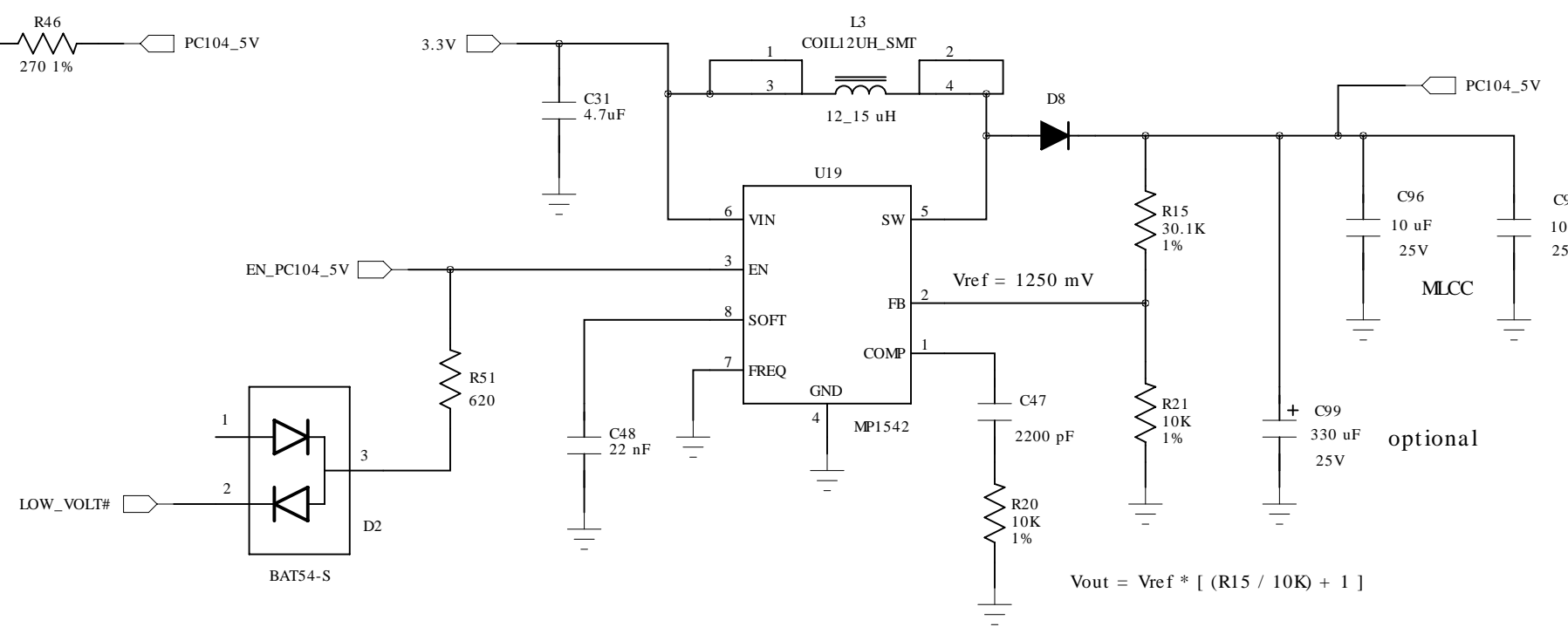
### 4.5-20V Power In



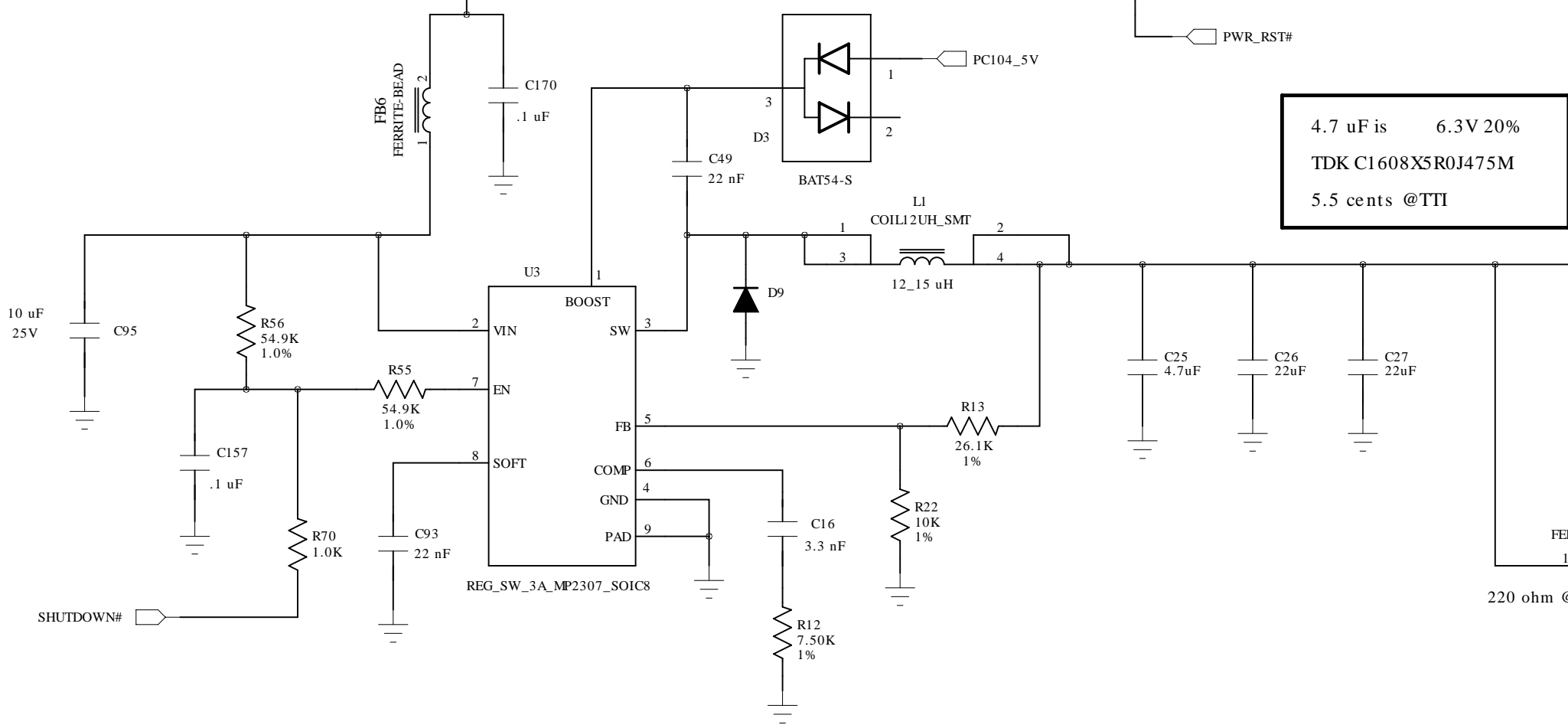
### 5V Load



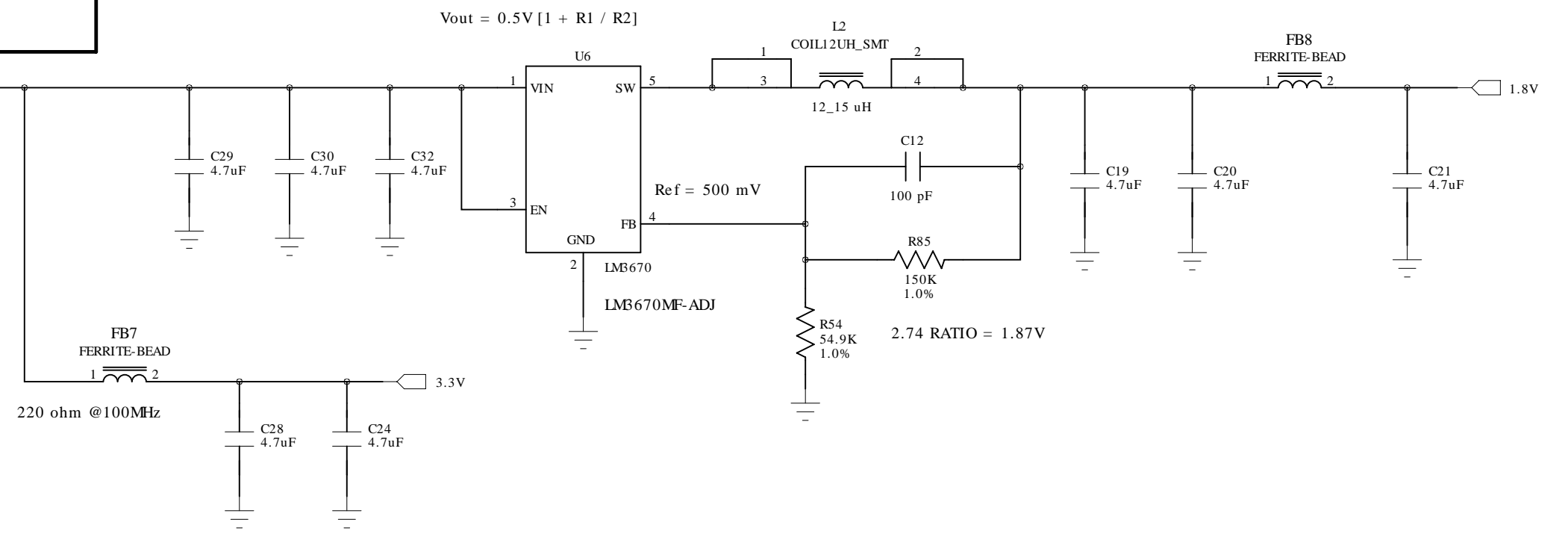
### 5V Boost Supply



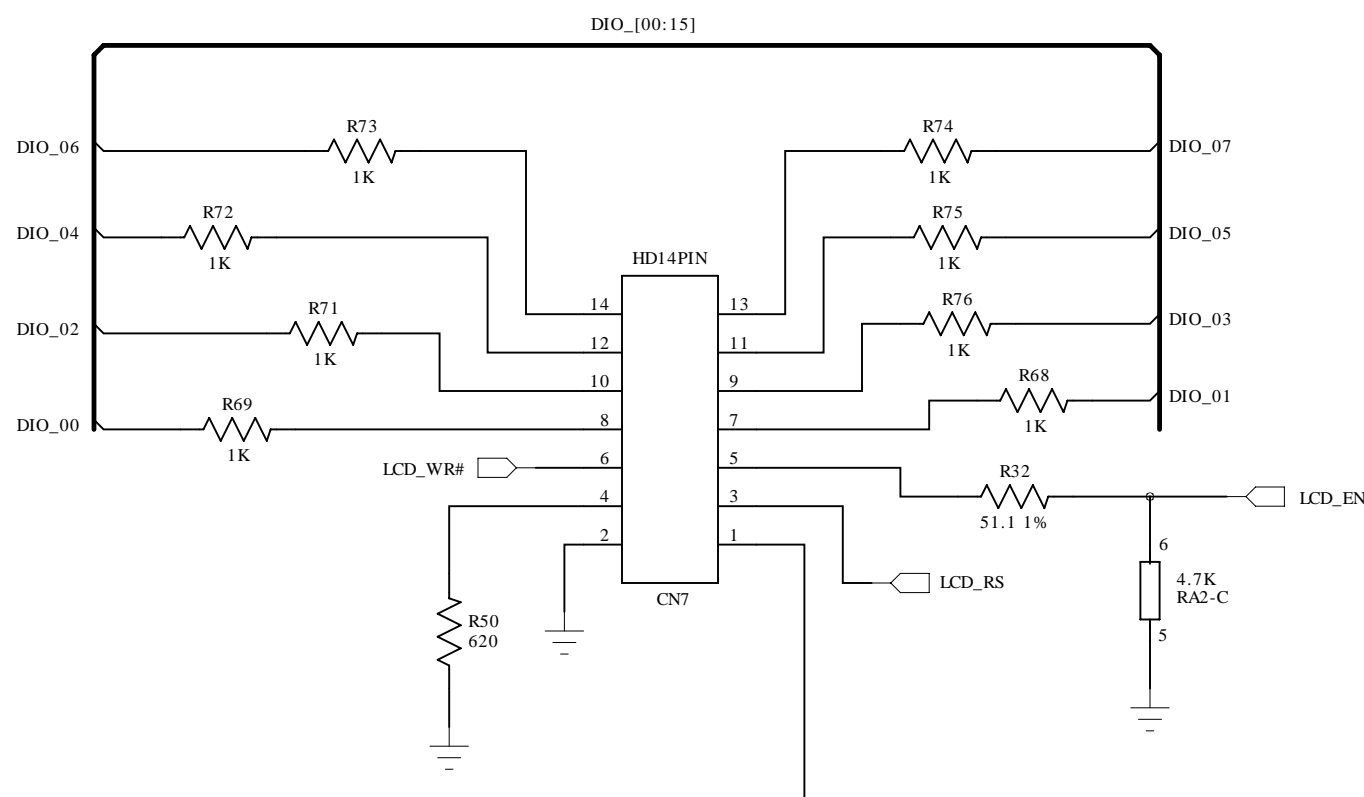
### 3.3V Power Supply



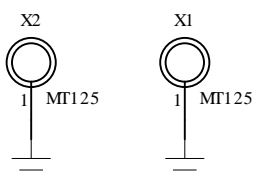
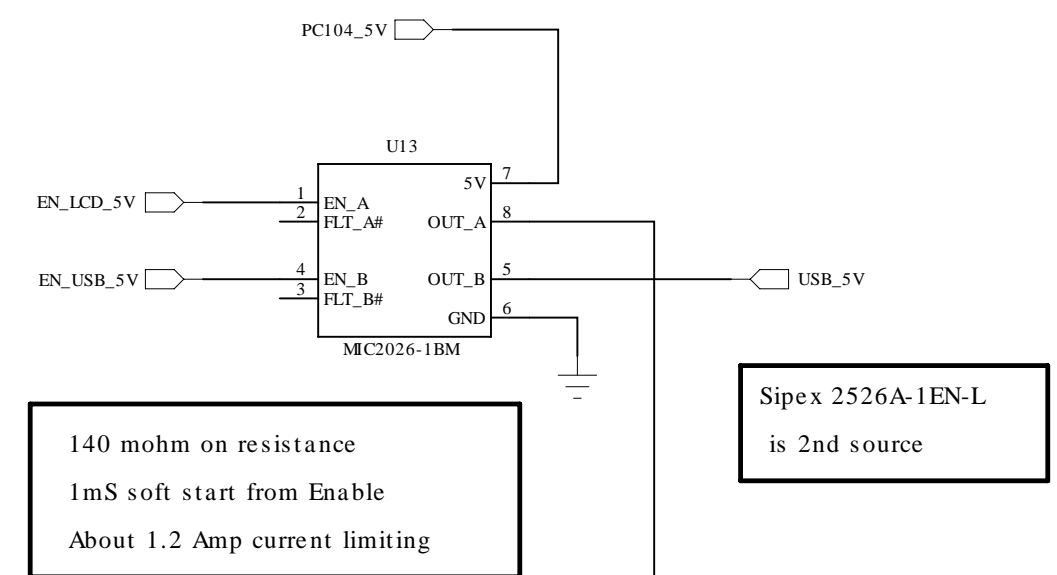
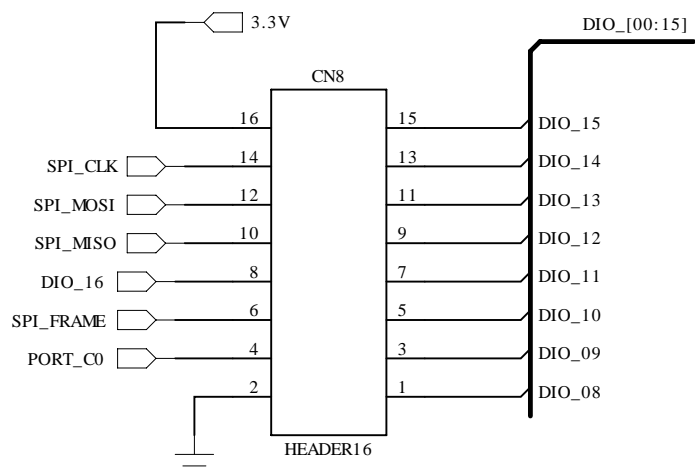
### 1.8V Power Supply



### LCD Port



### DIO Port



**Jumpers:**

- JP1 = Boot Serial
- JP2 = Console Enable
- JP3 = Write Enable Flash
- JP4 = COM2 is Console
- JP5 = TS\_Test
- JP6 = Reserved

The XDIO pins can optionally support these signals

- Pin 1 = COMB TXD
- Pin 3 = COMB RXD
- Pin 5 = Aux1 TXD
- Pin 7 = Aux1 RXD
- Pin 9 = Aux2 TXD
- Pin 11 = Aux2 RXD
- Pin 13 = Aux1 TX shifting
- Pin 15 = Aux2 TX shifting

The XDIO pins can optionally support an SD card socket

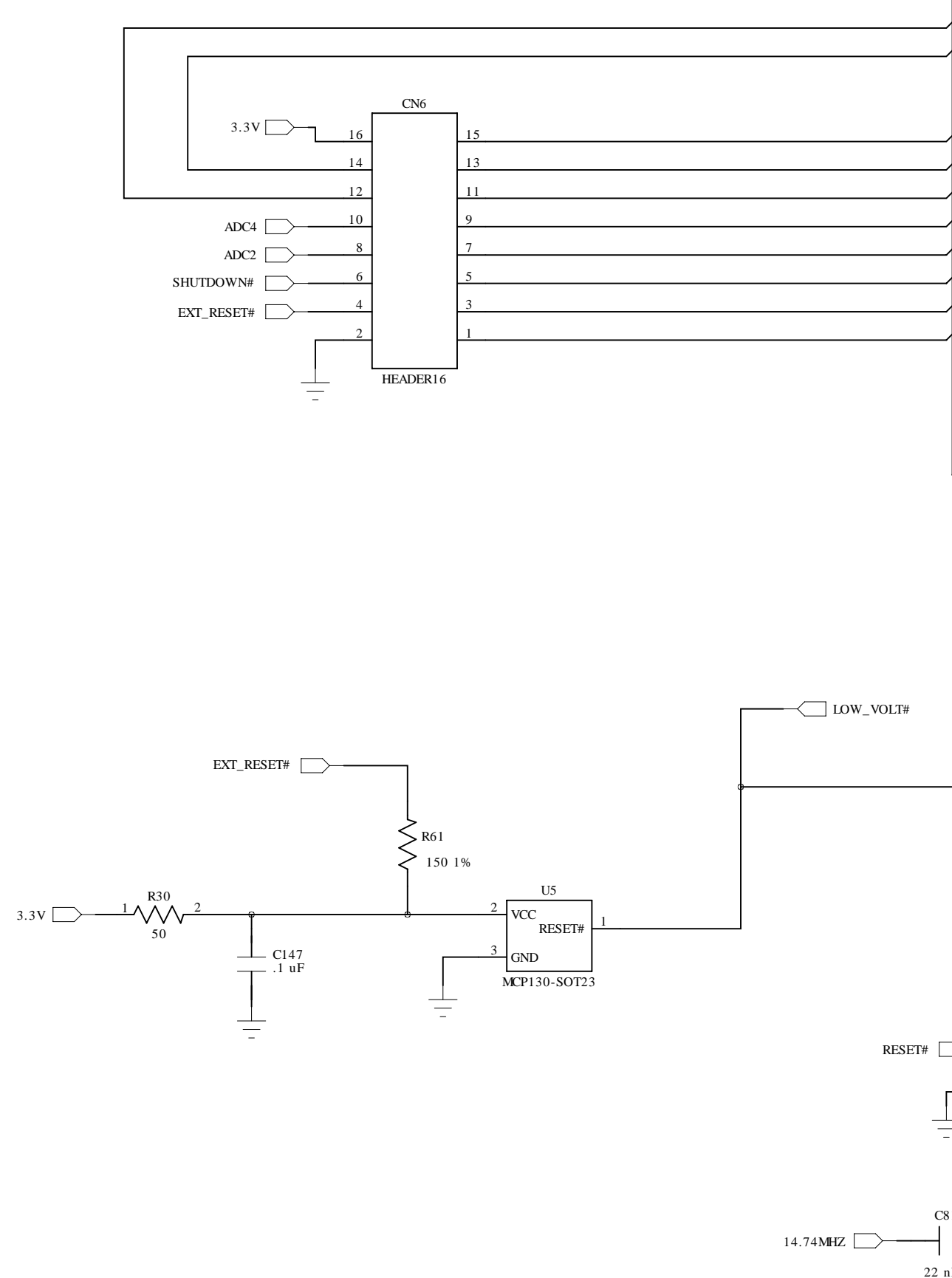
- Pin 1 = XDIO\_0 SD Data\_1
- Pin 3 = XDIO\_1 SD Data\_0
- Pin 5 = XDIO\_2 SD Command
- Pin 7 = XDIO\_3 SD Hard Power#
- Pin 9 = XDIO\_4 SD Data\_2
- Pin 11 = XDIO\_5 SD Data\_3
- Pin 13 = XDIO\_6 SD Clock
- Pin 15 = XDIO\_7 SD Present#
- Pin 14 = XDIO\_8 SD Write Prot.

All of these pins need the FPGA pull-up resistor turned on except the SD Clock signal and SD Hard Power# signal

MAX2\_570 requires 300 uS to copy  
Flash into RAM after Vcore > 1.5V

XDIO\_DIO9 also drives COMB\_RTS  
XDIO\_DIO9 is always an output

**DIO2 Port**



**JTAG**

