Lattice XP2 FPGA

PROC/CE, and NCE are dedicated configuration pins where CE3 is low. When CE3 is high, they are 'gated' power-up STDX. Page 8 of [DOC]

During FPGA Flash programming, the PROC/CE must remain high and NCE can switch from --- MISO DOUT OE/SOE must be high
Those are to help with PC-resistors

Note CE and NCE must be added to --- 3.3k suggested

Note CE must be a chip select (or low) DOUT = Self-charged Move
SDO uses an internal pull --- 3.3 k
DQG (DIOL) resistor case XG, some [DOC]

16K Memory is 7P package from [DOC]
Referenced to this FPGA part can be written as used per normal package for ADC, with ease. However, if 16K memory can be accessed from reads

Set CE, NCE, DIOL to DNOE

Full-up and pull-down resistors are 10 to 200k ohm

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Title: TS-7390 FPGA and Frame Buffer
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Hysteretic Switching Power Supply

5V @ (2.5 Amps)

5-28V Power In

By Technologic Systems Date: Aug. 11, 2008
Title: TS-7390 5V Power Supply
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