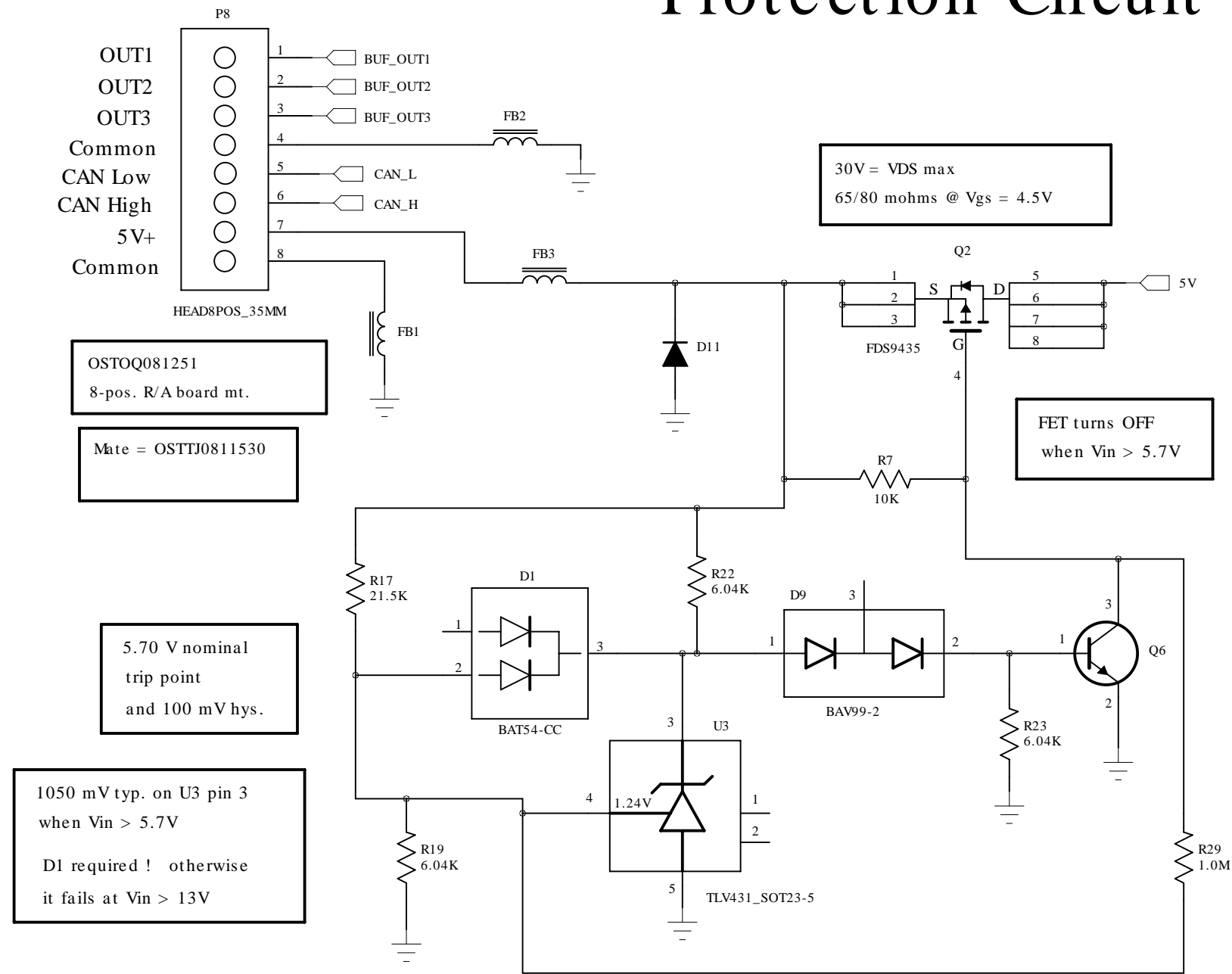


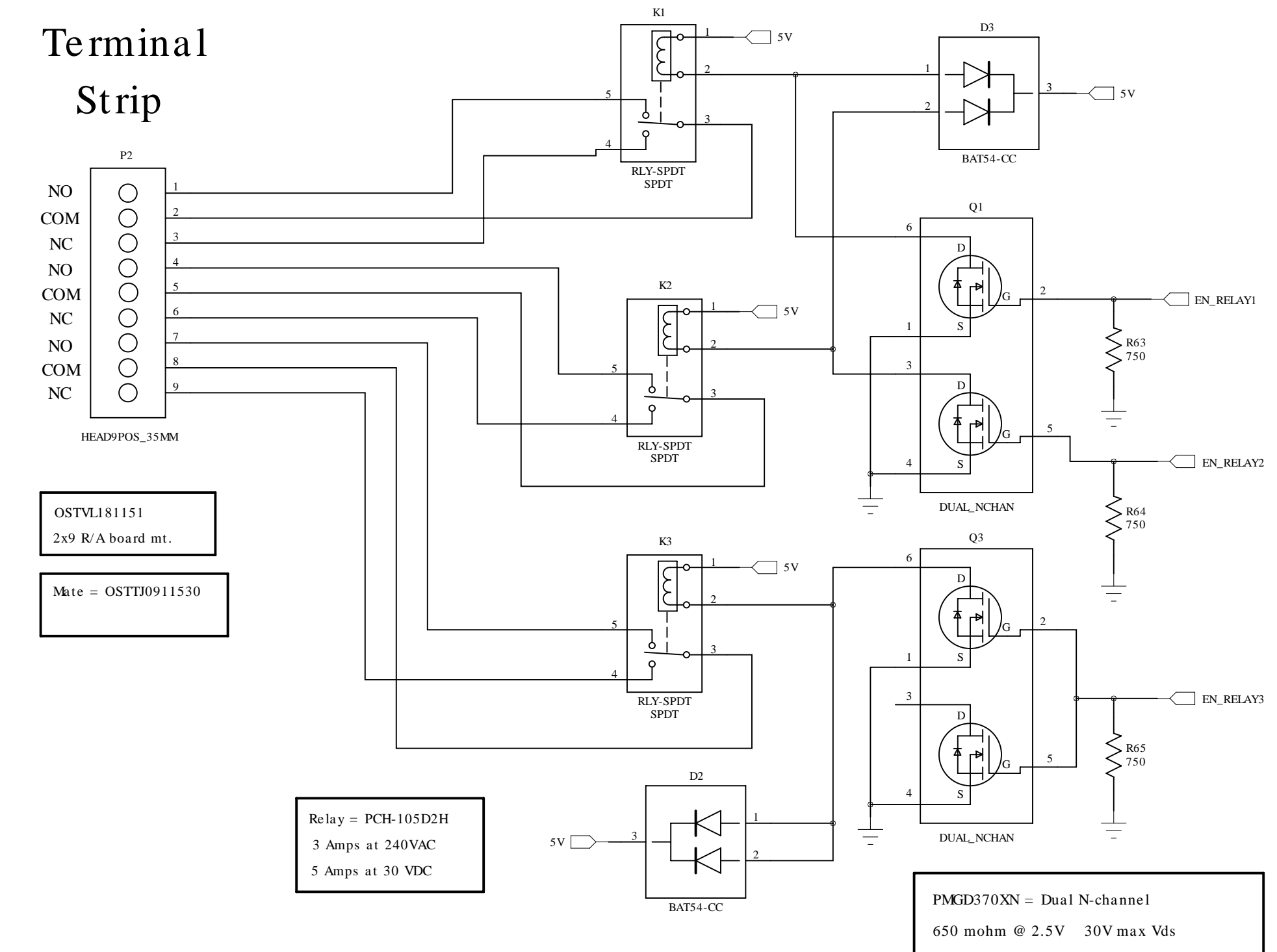
8 Position Terminal Strip

Over Voltage Protection Circuit



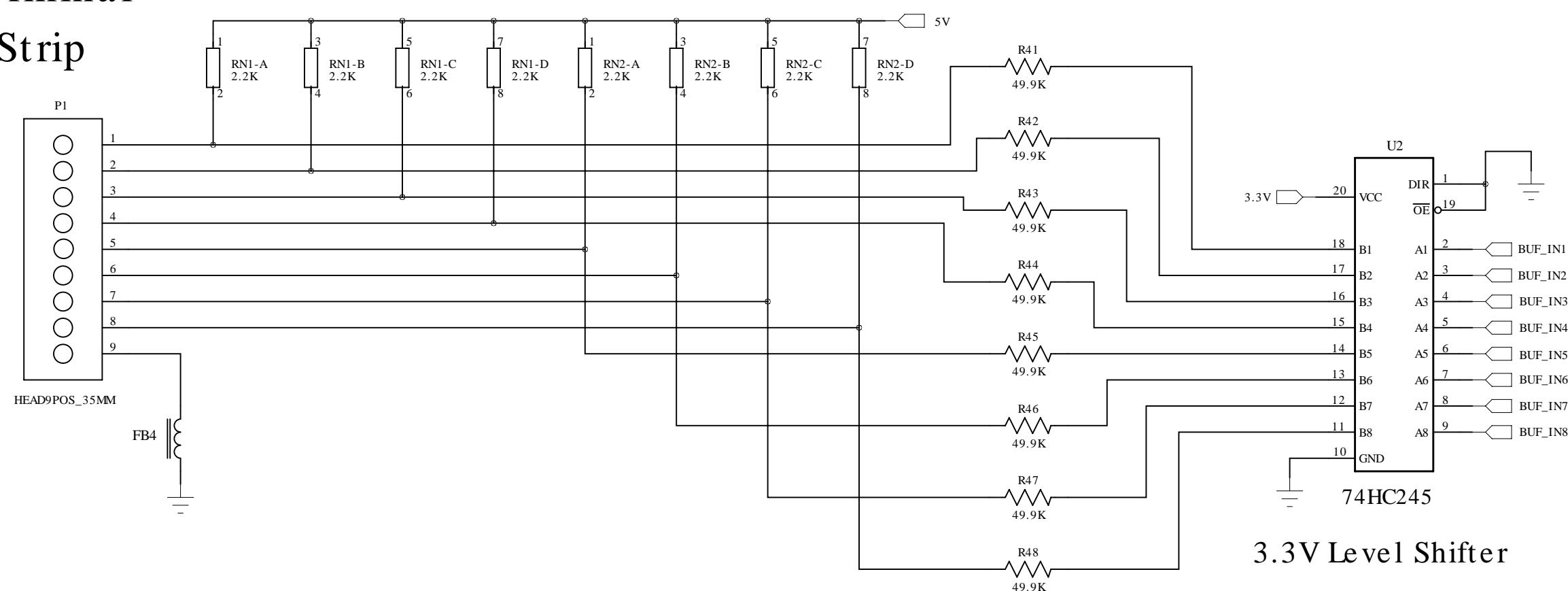
Top 9 Position Terminal Strip

Relays



Bottom 9 Position Terminal Strip

System ReBoot

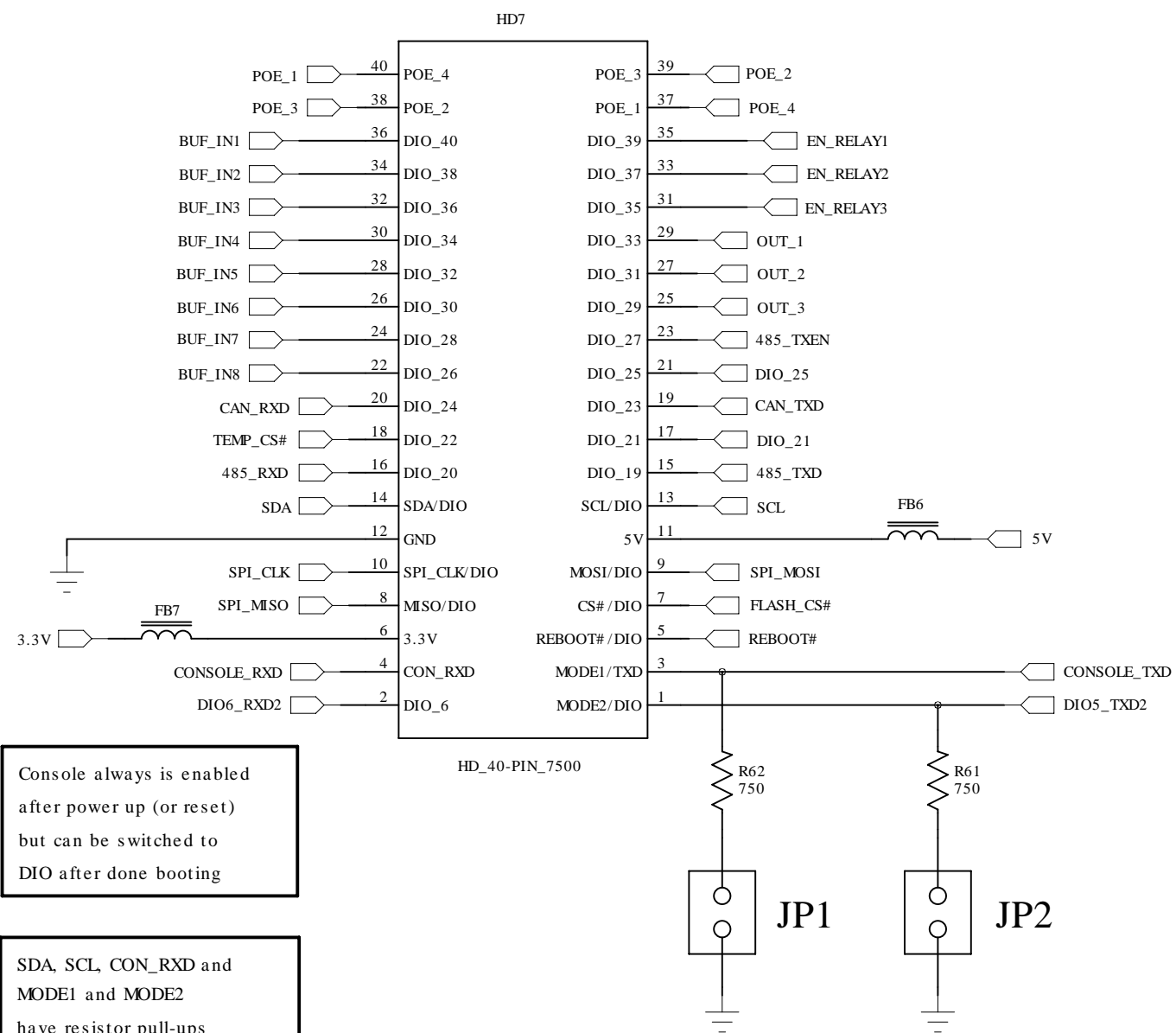


System ReBoot

Technologic Systems	Date June 2, 2009
Title: TS-752 -- Base Bd. in TS-7500 Enclosure	
Rev:	Designer RLM Sheet 1 of 4

40-pin Connector

To 7500



Console always is enabled after power up (or reset) but can be switched to DIO after done booting

SDA, SCL, CON_RXD and MODE1 and MODE2 have resistor pull-ups on TS-7500 and TS-7550

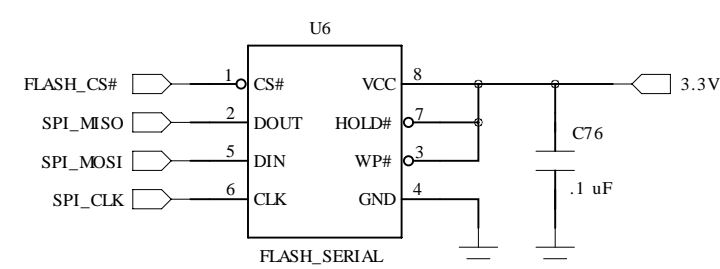
Reboot# pin (DIO_09) defaults to DIO But can be switched to Reset function

SPI bus default to DIO But can be switched to SPI function

Mode 1	Mode 2	TS-7500 Boots from	TS-7550 Boots from
1	1	SPI Flash	NAND Flash
0	1	SD card	Reserved
1	0	TS-752 SPI Flash	TS-752 SPI Flash
0	0	Reserved	Reserved

0 0 also boots from TS-752 Flash, but may be changed in the future

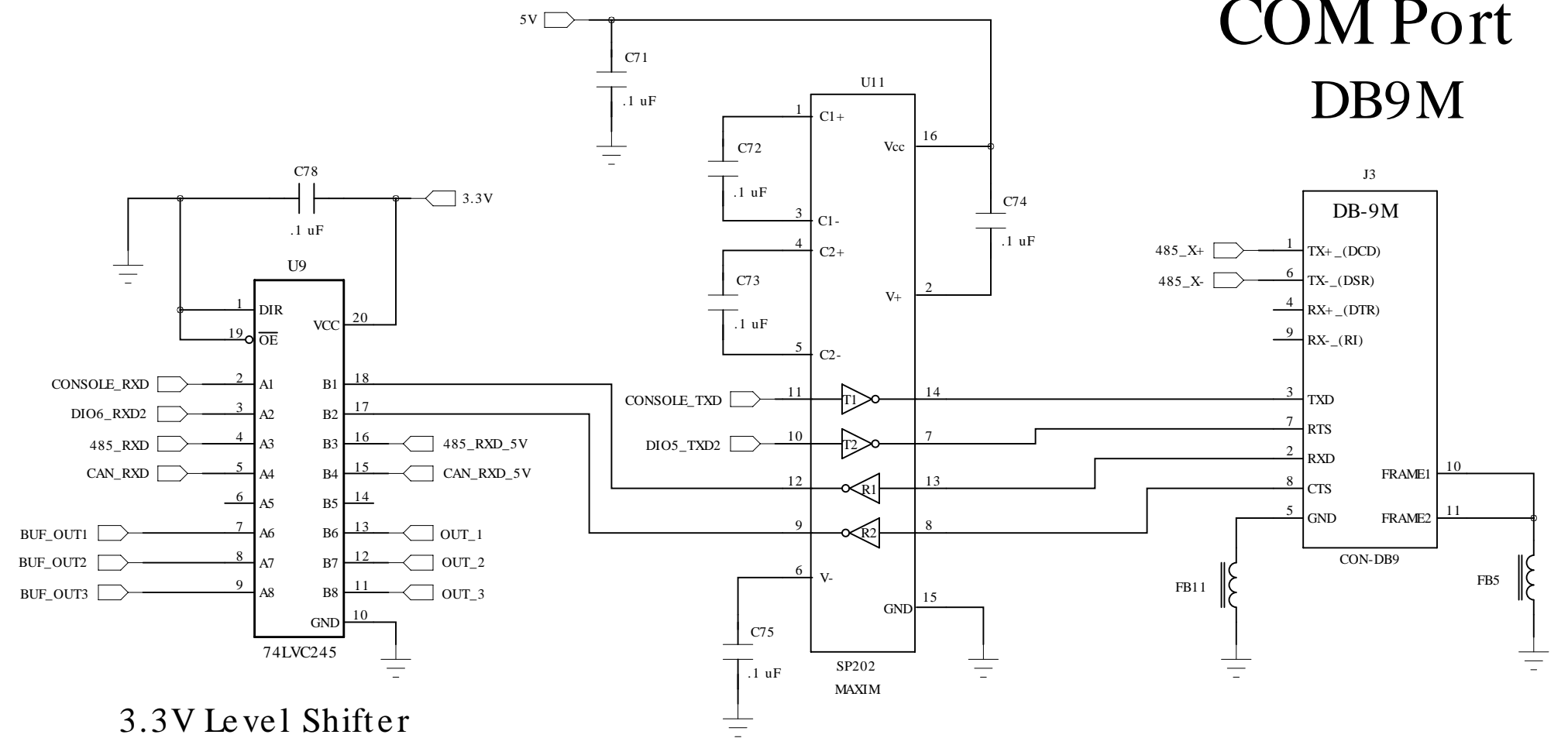
Serial Flash



For Recovery only

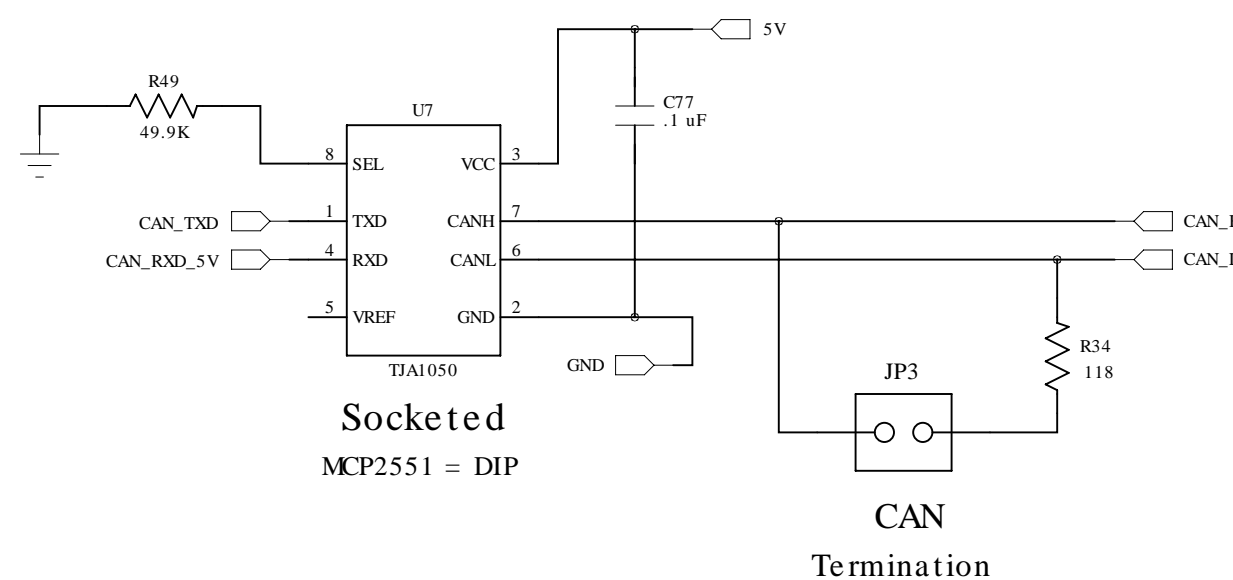
Used to De-Brick a TS-75xx

RS-232 Transceiver



3.3V Level Shifter

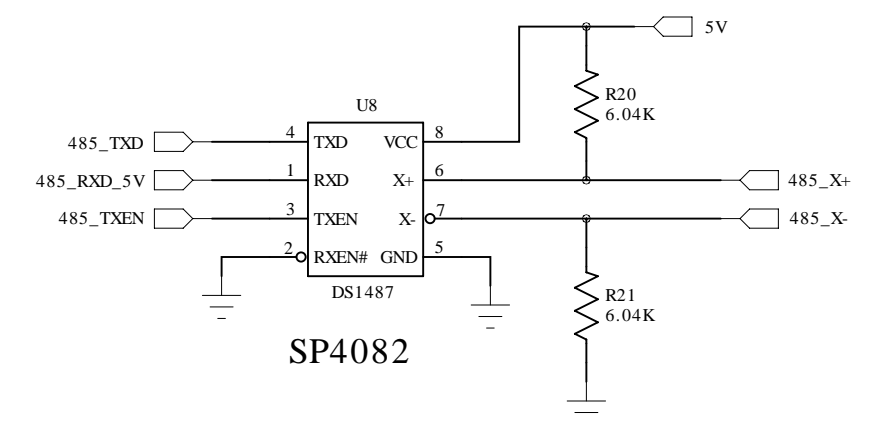
CAN Transceiver



Socketed MCP2551 = DIP

CAN Termination

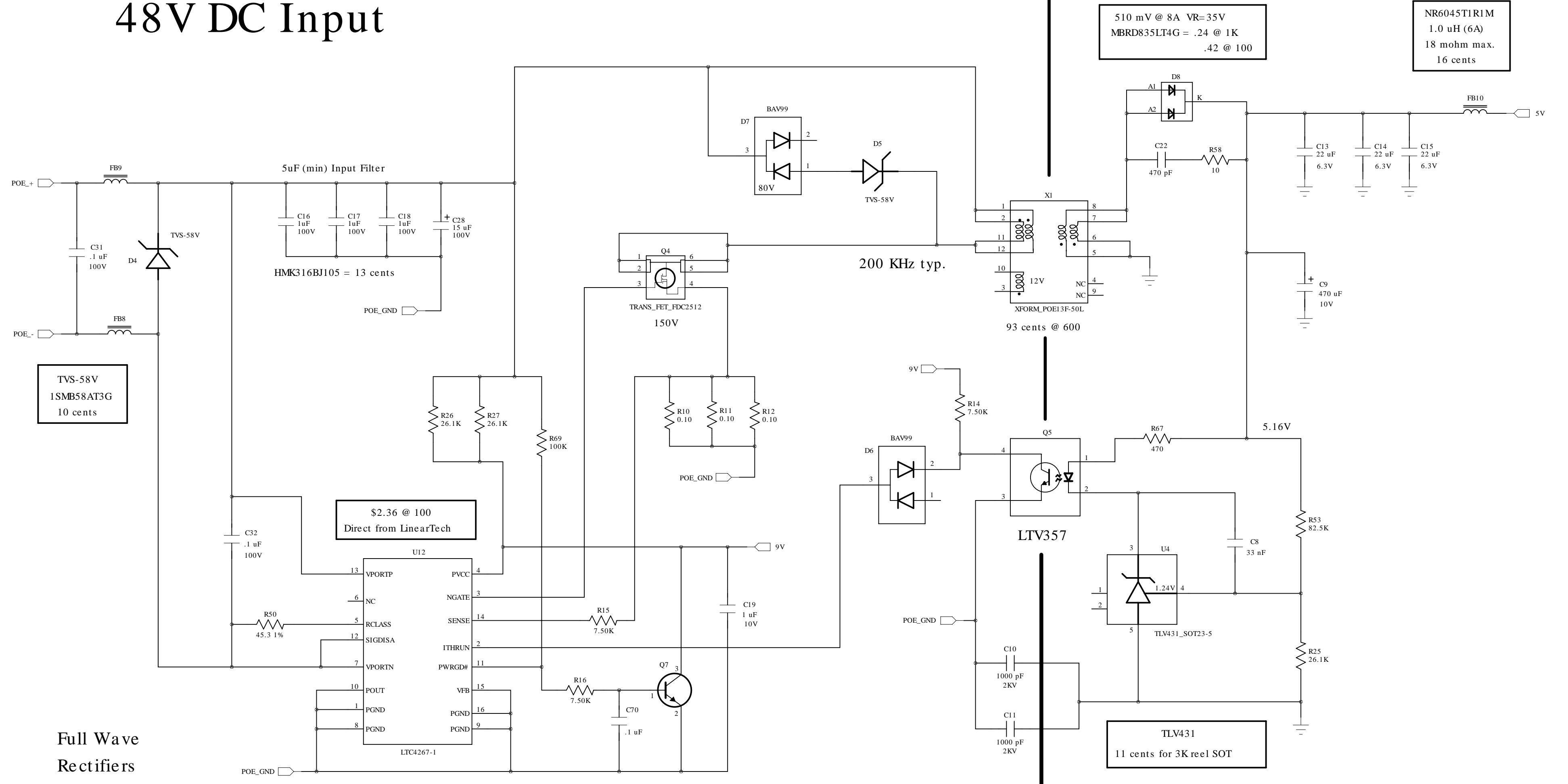
RS-485 Transceiver



Technologic Systems	Date	June 2, 2009
Title: TS-752 -- Base Bd. in TS-7500 Enclosure		
Rev:	Designer	RLM Sheet 2 of 4

POE Side 48V DC Input

Reg. 5V Out



TVS-58V
1SMB58AT3G
10 cents

HMK316BJ105 = 13 cents

\$2.36 @ 100
Direct from LinearTech

510 mV @ 8A VR=35V
MBRD835LT4G = .24 @ 1K
.42 @ 100

NR6045T1R1M
1.0 uH (6A)
18 mohm max.
16 cents

200 KHz typ.

93 cents @ 600

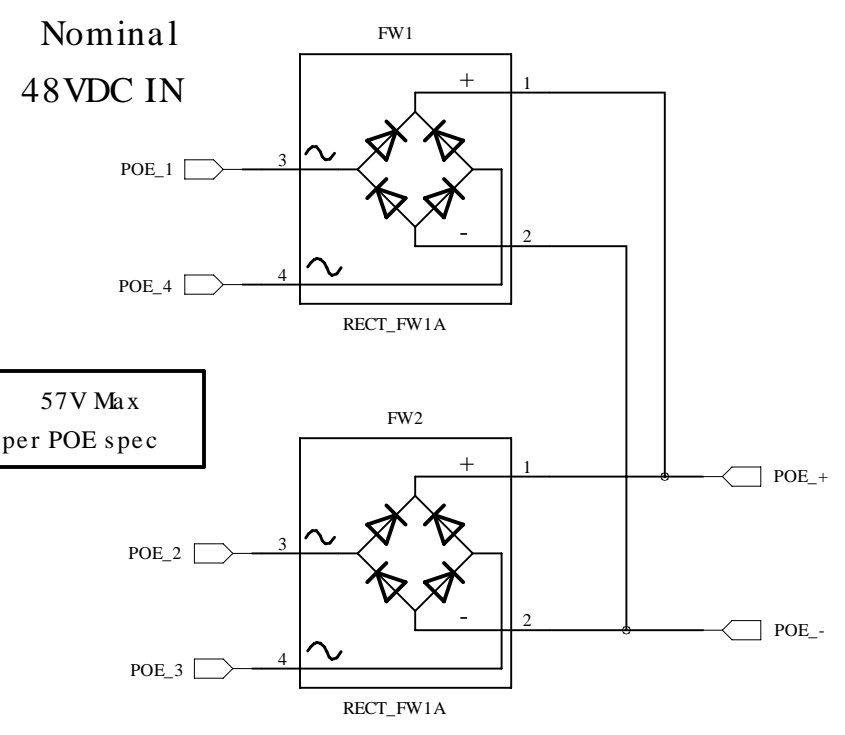
LTV357

TLV431
11 cents for 3K reel SOT

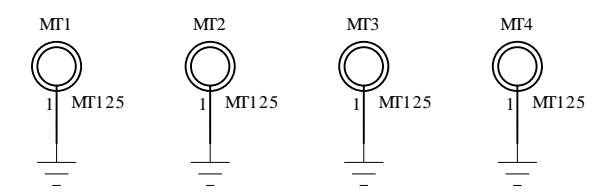
202R18W102KV4E
1000 pF @ 2KV
4.5 cents @ 3K

Power Good Circuit
is optional

Full Wave Rectifiers

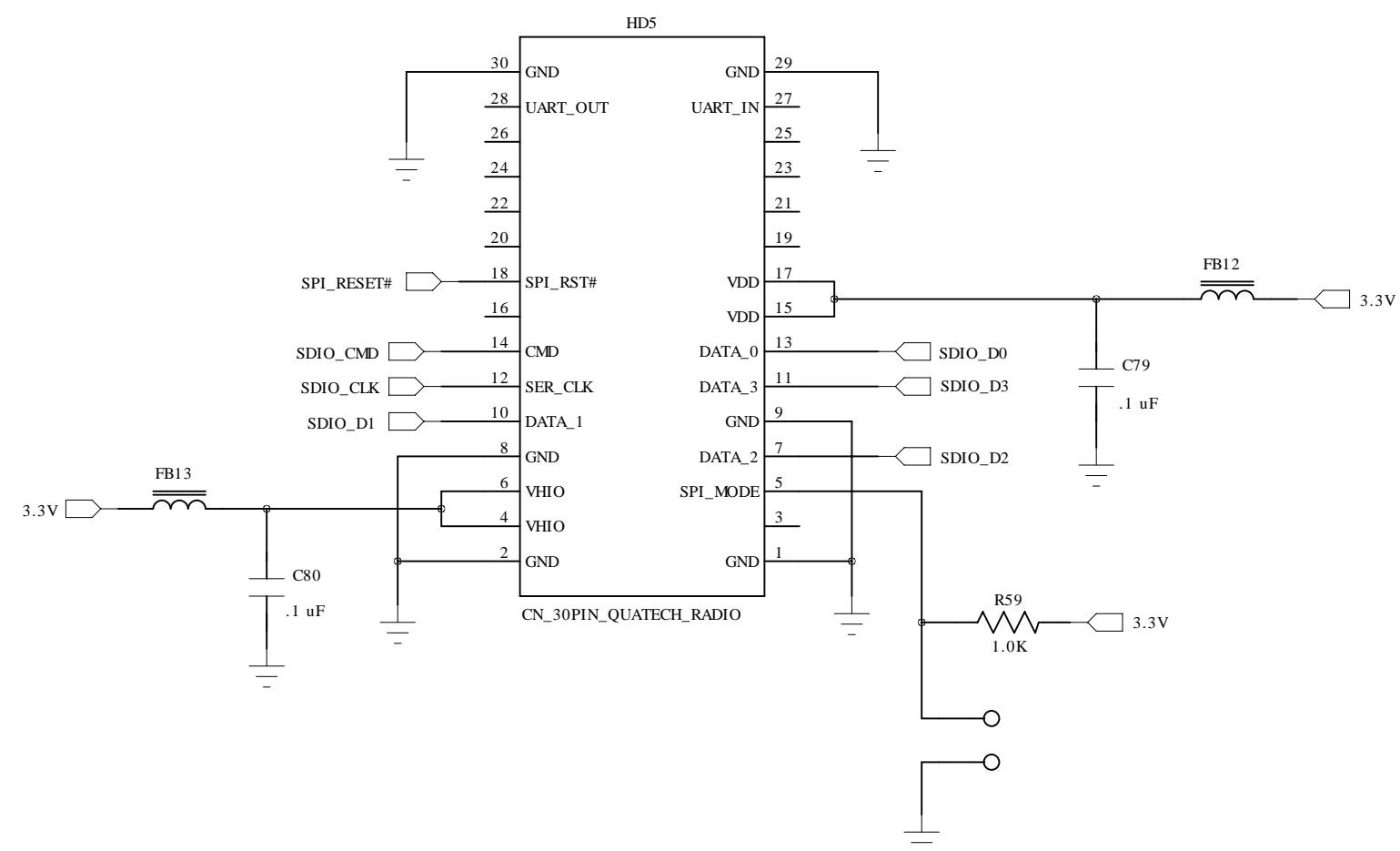


57V Max
per POE spec



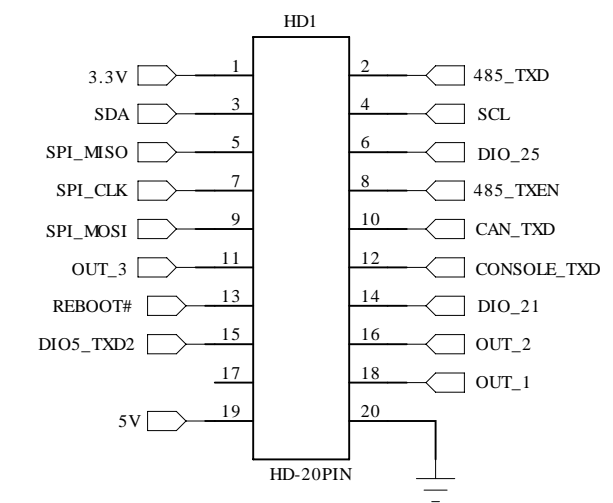
Technologic Systems		Date	June 2, 2009
Title: TS-752 -- Base Bd. in TS-7500 Enclosure			
Rev: P	Designer RLM	Sheet 3 of 4	

QuaTech WiFi Radio



SPI_MODE at GND = SPI mode
Logic high = SDIO mode

DIO Header



Temp Sensor

