**Title:** Design Sheet

**Date:** June 2, 2009

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**40-pin Connector**

- To 7500
- RS-232 Transceiver
- COM Port DB9M
- CAN Transceiver
- RS-485 Transceiver

---

**For Recovery only**

Used to De-Brick a TS-75xx

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**Serial Flash**

**40-pin Connector**

**RS-232 Transceiver**

**COM Port DB9M**

**CAN Transceiver**

**RS-485 Transceiver**

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**3.3V Level Shifter**

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**Technologic Systems**

**Date:** June 2, 2009

**Title:** TS-752 - Base Bd. in TS-7500 Enclosure

**Rev:**

**Designer:**

**RIM:**

**Sheet:** 2 of 4

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**Console always is enabled after power up (or reset) but can be switched to DIO after done booting**

- SPI, SCL, CON, SMD and MODE1 and MODE2 have receiver pull-ups on TS-7500 and TS-7550

- Reset pin (MODE0) defaults to DIO but can be switched to Reset function.

- SPI bus default is DIO but can be switched to SPI function.

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**Mode 1**

<table>
<thead>
<tr>
<th>Mode 1</th>
<th>Mode 2</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
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</tr>
<tr>
<td>0</td>
<td>1</td>
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<tr>
<td>1</td>
<td>0</td>
</tr>
<tr>
<td>0</td>
<td>0</td>
</tr>
</tbody>
</table>

**TS-7500**

- SPI Flash
- SD card
- TS-752 SPI Flash
- Reserved
- Reserved

**TS-7550**

- SPI Flash
- SD card
- Reserved
- Reserved

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**3.3V also boots TS-752 Flash, but may be changed in the future**
POE Side
48V DC Input

5uF (min) Input Filter

Full Wave Rectifiers
Nominal 48VDC IN

Power Good Circuit is optional

Reg. 5V Out

500 mV @ 8A VM: 5V
MBR6527L/P6G < 2A @ 1.5K
42 @ 100

NR0147T/L1M
3.8K ohm (0A)
18 mohm max.
16 cents

Technologic Systems
Title: TS-752 - Base Bd. in TS-7500 Enclosure
Date: June 2, 2009
Rev: P
Designer: RLM
Sheet 3 of 4
QuaTech WiFi Radio

Temp Sensor

DIO Header

QuaTech WiFi Radio

Temp Sensor

DIO Header

QuaTech WiFi Radio

Temp Sensor

DIO Header

QuaTech WiFi Radio

Temp Sensor

DIO Header