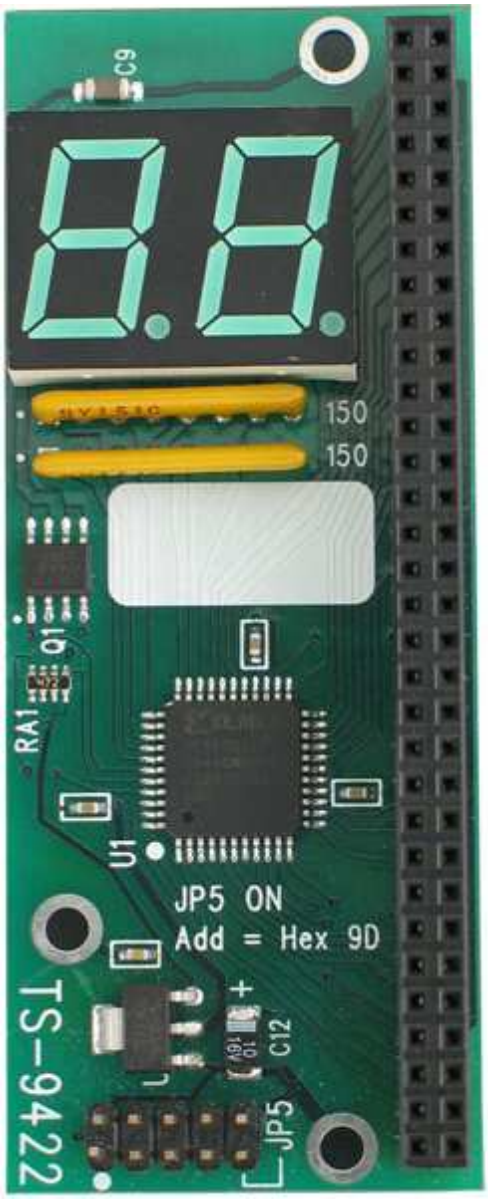
OVERVIEW

TS-9422 is a software debugging tool for use on any PC/104 system. It displays POST codes on two 7-segment LEDs. The default I/O location is the PC standard location of Hex 80. By installing jumper # JP5, the decoded I/O location is changed to location Hex 9D. This provides a unique address for the user's program to use during debug. This peripheral is designed for use with Technologic Systems' x86 SBC's.

- ✔ Two 7-segment LEDs
- ✔ PC/104 8-bit bus
- ✔ Standard PC I/O address
- ✔ Jumper selectable I/O address
- ✔ Dimensions 3.375x1.375 inches

HARDWARE CONFIGURATION

TS-9442 board's front view



Usage

Insert the TS-9422 in to the SBC it is to be used with, making sure to align all of the pins as well as taking note of the orientation using the mounting holes. Inserting the peripheral board backwards may cause damage. The two seven segment display will display the byte wide value stored at I/O address 80h which is used in x86 to display POST codes. The TS-9422 will display values at 9Dh if JP5 is installed.