System Level Block Diagram
TS-4900 COM

Freescale i.MX6

Options:

<table>
<thead>
<tr>
<th>CPU</th>
<th>Temp.</th>
<th>RAM</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single Core 800MHz</td>
<td>-40 to 85C</td>
<td>1 GByte</td>
</tr>
<tr>
<td>Single Core 1GHz</td>
<td>0 to 70C</td>
<td>1 GByte</td>
</tr>
<tr>
<td>Quad Core 1GHz</td>
<td>-20 to 70C</td>
<td>2 GByte</td>
</tr>
</tbody>
</table>

Micro SD Card Socket
4GByte eMMC
DDR3 RAM
SPI Boot Flash

Ethernet PHY 10M/100M/1000M
SD Card
10/100/1000 Base-T
SATA
PCIe
USB
USB OTG
24-bit LCD
16-bit Parallel
I2C
I2S
CAN (2)
SPI (2)
GPIO
UART (5)

16-bit Parallel
24-bit LCD

Input Power
Output Power
GND
5V
RTC Bat.
3.3V

WiFi 802.11 b/g/n + Bluetooth 4.0/BLE Radio
Real Time Clock

Technology Systems 01/29/15

--- Optional Feature ---